```
           RWErrorRecovery Mode Page
              RWErrorRecovery Group
Byte:009A:       RWRecoveryFlags = C0
Byte:009A:           Bit:0, DISABLE_CORRECTION = 0
Byte:009A:           Bit:1, DISABLE_TRANSFER_ON_ERROR = 0
Byte:009A:           Bit:2, POST_ERROR = 0
Byte:009A:           Bit:3, ENABLE_EARLY_RECOVERY = 0
Byte:009A:           Bit:4, READ_CONTINUOUS = 0
Byte:009A:           Bit:5, TRANSFER_BLOCK = 0
Byte:009A:           Bit:6, READ_SPARING_ENABLED = 1
Byte:009A:           Bit:7, WRITE_SPARING_ENABLED = 1
Byte:009B:       ReadRetries = 10
Byte:009C:       CorrectionSpan = FF
Byte:009D:       HeadOffset = 00
Byte:009E:       DataStrobeOffset = 00
Byte:00A0:       WriteRetries = 05
Byte:00A2:       RecoveryLimitMSB = FF
Byte:00A3:       RecoveryLimitLSB = FF

           FormatParameters Mode Page
              FormatParameters Group
Byte:00A6:       TracksPerZoneMSB = 00
Byte:00A7:       TracksPerZoneLSB = 01
Byte:00A8:       AltSectorsPerZoneMSB = 00
Byte:00A9:       AltSectorsPerZoneLSB = 00
Byte:00AA:       AltTracksPerZoneMSB = 00
Byte:00AB:       AltTracksPerZoneLSB = 00
Byte:00AC:       AltTracksPerVolumeMSB = 00
Byte:00AD:       AltTracksPerVolumeLSB = 02
Byte:00AE:       SectorsPerTrackMSB = 00
Byte:00AF:       SectorsPerTrackLSB = 03
Byte:00B0:       BytesPerSectorMSB = 02
Byte:00B1:       BytesPerSectorLSB = 00
Byte:00B2:       InterleaveMSB = 00
Byte:00B3:       InterleaveLSB = 01
Byte:00B4:       TrackSkewMSB = 00
Byte:00B5:       TrackSkewLSB = 00
Byte:00B6:       CylinderSkewMSB = 00
Byte:00B7:       CylinderSkewLSB = 00
Byte:00B8:       FormatFlags = 40

           DriveGeometry Mode Page
              DriveGeometry Group
Byte:00BE:       CylindersMSB = 00
Byte:00BF:       CylindersMDB = 00
Byte:00C0:       CylindersLSB = 04
Byte:00C1:       Heads = 04
Byte:00C2:       WritePrecompMSB = 00
Byte:00C3:       WritePrecompMDB = 00
Byte:00C4:       WritePrecompLSB = 00
Byte:00C5:       ReducedCurrentMSB = 00
Byte:00C6:       ReducedCurrentMDB = 00
Byte:00C7:       ReducedCurrentLSB = 00
Byte:00C8:       StepRateMSB = 00
Byte:00C9:       StepRateLSB = 00
```

```
Byte:00CA:       LandingZoneMSB = 00
Byte:00CB:       LandingZoneMDB = 00
Byte:00CC:       LandingZoneLSB = 00
Byte:00CD:       PositionLocking = 00
Byte:00CE:       RotationalOffset = 00
Byte:00D0:       RotationRateMSB = 15
Byte:00D1:       RotationRateLSB = 18

           VerifyError Mode Page
              VerifyError Group
Byte:00D6:       VerifyRecoveryFlags = 00
Byte:00D6:           Bit:0, VE_DISABLE_CORRECTION = 0
Byte:00D6:           Bit:1, VE_DISABLE_TRANSFER_ON_ERROR = 0
Byte:00D6:           Bit:2, VE_POST_ERROR = 0
Byte:00D6:           Bit:3, VE_ENABLE_EARLY_RECOVERY = 0
Byte:00D7:       VerifyRetryCount = 10
Byte:00D8:       VerifyCorrectionSpan = FF
Byte:00DE:       VerifyTimeLimitMSB = FF
Byte:00DF:       VerifyTimeLimitLSB = FF

           CacheControl Mode Page
              CacheControl Group
Byte:00E2:       CacheFlags = 14
Byte:00E2:           Bit:0, READ_CACHING_DISABLED_ON_POWER_UP = 0
Byte:00E2:           Bit:1, MULTIPLICATION_FACTOR = 0
Byte:00E2:           Bit:2, WRITE_CACHING_ENABLED_ON_POWER_UP = 1
Byte:00E2:           Bit:4, DISCONTINUITY = 1
Byte:00E2:           Bit:5, CACHING_ANALYSIS_PERMITTED = 0
Byte:00E2:           Bit:6, ABORT_PREFETCH = 0
Byte:00E2:           Bit:7, DISABLE_ADAPTIVE_READ_AHEAD = 0
Byte:00E3:       RetentionPriority = 00
Byte:00E4:       DisablePrefetchLengthMSB = FF
Byte:00E5:       DisablePrefetchLengthLSB = FF
Byte:00E6:       MinPrefetchMSB = 00
Byte:00E7:       MinPrefetchLSB = 00
Byte:00E8:       MaxPrefetchMSB = FF
Byte:00E9:       MaxPrefetchLSB = FF
Byte:00EA:       MaxPrefetchCeilingMSB = FF
Byte:00EB:       MaxPrefetchCeilingLSB = FF
Byte:00EC:       SpecialCacheFlags = 80
Byte:00EC:           Bit:5, READ_LOOKAHEAD_DISABLED_ON_POWER_UP = 0
Byte:00EC:           Bit:7, FORCE_SEQUENTIAL_WRITE = 1
Byte:00ED:       CacheSegmentNum = 20
Byte:00EE:       CacheSegSizeMSB = 00
Byte:00EF:       CacheSegSizeLSB = 00
Byte:00F1:       NonCacheSegSizeMSB = 00
Byte:00F2:       NonCacheSegSizeMDB = 00
Byte:00F3:       NonCacheSegSizeLSB = 00

           ControlMode Mode Page
              ControlMode Group
Byte:00F6:       LogFlags = 02
Byte:00F7:       QueueFlags = 00
Byte:00F8:       AllegianceFlags = 00
Byte:00FA:       RAERHoldOffMSB = 00
Byte:00FB:       RAERHoldOffLSB = 00
Byte:00FC:       BusyTimeoutMSB = 00
```

```
Byte:00FD:       BusyTimeoutLSB = 00
Byte:00FE:       ExtendedDstestTimeMSB = 00
Byte:00FF:       ExtendedDstestTimeLSB = 00

           PowerCondition Mode Page
              PowerCondition Group
Byte:0103:       PowerFlags = 02
Byte:0103:           Bit:0, STANDBY = 0
Byte:0103:           Bit:1, IDLE = 1
Byte:0104:       IdleTimerMSB = 00
Byte:0105:       IdleTimerNMSB = 00
Byte:0106:       IdleTimerNLSB = 00
Byte:0107:       IdleTimerLSB = 05
Byte:0108:       StandbyTimerMSB = 00
Byte:0109:       StandbyTimerNMSB = 00
Byte:010A:       StandbyTimerNLSB = 00
Byte:010B:       StandbyTimerLSB = 04

           BackgroundMediaScan Mode Page
              BGMS Group
Byte:010F:       BGMSBusIdleIn100ms = 00
Byte:0110:       OtherScanFlags = 00
Byte:0110:           Bit:0, IRAW_ENABLE = 0
Byte:0110:           Bit:1, IRAW_DISABLE_REASSIGNMENTS = 0
Byte:0111:       IRAWWriteCachePercentage = 00
Byte:0112:       IRAWDelayInMilliSecs = 00
Byte:0113:       IRAWMaxQDepth = 00
Byte:0114:       BGMSFlags = 00
Byte:0114:           Bit:1, BGMS_DISABLE_DATA_REFRESH = 0
Byte:0114:           Bit:2, BGMS_DISABLE_TEMPERATURE_LIMITATION = 0
Byte:0115:       BGMSECCTLevel = 08
Byte:011C:       ReadAfterWriteControl = 31
Byte:011C:           Bit:0, MP_RAW_ENABLE_BIT = 1
Byte:011C:           Bit:1, MP_RAW_TRIP_1ST_N_WRITES = 0
Byte:011C:           Bit:2, MP_RAW_FORCE_RAW_MODE = 0
Byte:011C:           Bit:3, MP_RAW_UDS_DEBUG_MODE = 0
Byte:011C:           Bit:4, MP_RAW_DISABLE_AR_ON_GOOD_READ = 1
Byte:011C:           Bit:5, MP_RAW_DISABLE_SOFT_AR = 1
Byte:011C:           Bit:6, MP_RAW_DISABLE_SERIAL_DEBUG_MSG = 0
Byte:011D:       NWritesAfterSWDFail = 08
Byte:011E:       NWritesAfterRAWFail = 08
Byte:011F:       NWritesAfterIRAWFail = 08
Byte:0120:       N1stWrites = 00
Byte:0121:       RAWOnColdThreshold = 0A
Byte:0122:       RAWOnHotThreshold = 3A
Byte:0123:       RAWATAMode = 02
Byte:0124:       RAWVerifyNSectors = 00 00 01 00
Byte:0128:       RAWVerifyNSectorsATAMode3 = 00 00
Byte:012A:       NWritesAfterSWDSumFail = 08
Byte:012C:       SWDControl = 00
Byte:012C:           Bit:0, MP_SWD_ENABLE_BIT = 0
Byte:012C:           Bit:1, MP_SWD_DONT_REPORT_BIT = 0
Byte:012C:           Bit:2, MP_SWD_DEBUG_MODE_BIT = 0
Byte:012D:       SWDDvgasEventsBeforeFail = 00
Byte:012E:       SWDRvgasEventsBeforeFail = 00
Byte:012F:       SWDFvgasEventsBeforeFail = 00
Byte:0130:       SWDSumOfEventsBeforeFail = 00
```

```
Byte:0134:       DOSOughtToScanThreshold = 02
Byte:0135:       DOSNeedToScanThreshold = 04
Byte:0136:       DOSNeedToGraceMilliseconds = 13 88
Byte:0138:       DOSNeedToRegraceMilliseconds = 03 E8
Byte:013A:       DOSNeedToMinimumScanMilliseconds = 00 C8
Byte:013C:       RAWOnForNPowerCycles = 00 00
Byte:013E:       RAWOnForNHours = 00 00
Byte:0140:       RAWOnForNSpinups = 00 00

          UnifiedDebugGeneral Mode Page
             UDSGeneral Group
Byte:0146:       Revision = 00
Byte:0147:       GlobalTraceTriggerFlags = 70
Byte:0147:           Bit:3, UDS_GEN_SVIN_BIT = 0
Byte:0147:           Bit:4, UDS_GEN_SVEN_BIT = 1
Byte:0147:           Bit:5, UDS_GEN_GTGE_BIT = 1
Byte:0147:           Bit:6, UDS_GEN_TREN_BIT = 1
Byte:0147:           Bit:7, UDS_GEN_TGTD_BIT = 0
Byte:0148:       MethodParameterFlags = 00
Byte:0148:           Bit:7, UDS_CLR = 0
Byte:014C:       DetailedExternalTriggerFlags = 00
Byte:014E:       ErrorTypeMSB = 00
Byte:014F:       ErrorTypeLSB = 00
Byte:0150:       ErrorLBABigEndian = 00 00 00 00 00 00 00 00
                 ''
Byte:015C:       AutomaticTraceSaveFlags = 80
Byte:015C:           Bit:5, UDS_GEN_INTR_BIT = 0
Byte:015C:           Bit:6, UDS_GEN_DTFS_BIT = 0
Byte:015C:           Bit:7, UDS_GEN_TSEN_BIT = 1
Byte:015E:       MSBAutomaticTraceSaveIntervalInMinutes = 00
Byte:015F:       LSBAutomaticTraceSaveIntervalInMinutes = 78
Byte:0160:       MSBMinimumIdleTimeBeforeAutoTraceSaveInMilliseconds = 00
Byte:0161:       LSBMinimumIdleTimeBeforeAutoTraceSaveInMilliseconds = C8
Byte:0162:       TimestampFormatIdentifierMSB = 00
Byte:0163:       TimestampFormatIdentifierLSB = 00
Byte:0164:       CustomerSystemTimeInCustUniqueFormat = 00 00 00 00 00 00 00 00
                 ''

          DriveNativeInfo Mode Page
             DriveNativeInfo Group
Byte:0186:       PartNumber = 30 32 37 4A 4A 30 44 54 58
                 '027JJ0DTX'
Byte:018F:       PartNumberWhitespacePad = 20 20 20 20 20 20 20 00
                 '       '
Byte:0197:       PartNumberPad = 00
Byte:0198:       ProcessControlVersion = 00 00
Byte:019A:       CongenConfigurationState = 01
Byte:019B:       WrittenCount = 00
Byte:019C:       InternalSeagateModelNumber =
                 53 54 39 35 30 30 33 32 35 41 53 20 20 20 20 20
                 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20
                 20 20 20 20 20 20 20 20
                 'ST9500325AS                                '
Byte:01C4:       BuildFlags = 00 00
Byte:01C4:           Bit:0, QNR_SUPPORTED = 0
Byte:01C6:       RealTimeUpdatedFlags = 08 00
Byte:01C6:           Bit:3, CONGEN_READ_FROM_MEDIA = 1
```

```
           UDSFastTraceClassesLevels Mode Page
              UDSFastTrace Group
Byte:01CC:       Revision = 00
Byte:01CD:       FastTraceLevels_0 = 11
Byte:01CE:       FastTraceLevels_1 = 11
Byte:01CF:       FastTraceLevels_2 = 11
Byte:01D0:       FastTraceLevels_3 = 11
Byte:01D1:       FastTraceLevels_4 = 11
Byte:01D2:       FastTraceLevels_5 = 11
Byte:01D3:       FastTraceLevels_6 = 11
Byte:01D4:       FastTraceLevels_7 = 12
Byte:01D5:       FastTraceLevels_8 = 11
Byte:01D6:       FastTraceLevels_9 = 11
Byte:01D7:       FastTraceLevels_10 = 11
Byte:01D8:       FastTraceLevels_11 = 11
Byte:01D9:       FastTraceLevels_12 = 11
Byte:01DA:       FastTraceLevels_13 = 11
Byte:01DB:       FastTraceLevels_14 = 11
Byte:01DC:       FastTraceLevels_15 = 11
Byte:01DD:       FastTraceLevels_16 = 11
Byte:01DE:       FastTraceLevels_17 = 11
Byte:01DF:       FastTraceLevels_18 = 11
Byte:01E0:       FastTraceLevels_19 = 11
Byte:01E1:       FastTraceLevels_20 = 11
Byte:01E2:       FastTraceLevels_21 = 11
Byte:01E3:       FastTraceLevels_22 = 11
Byte:01E4:       FastTraceLevels_23 = 11
Byte:01E5:       FastTraceLevels_24 = 11
Byte:01E6:       FastTraceLevels_25 = 11
Byte:01E7:       FastTraceLevels_26 = 11
Byte:01E8:       FastTraceLevels_27 = 11
Byte:01E9:       FastTraceLevels_28 = 11
Byte:01EA:       FastTraceLevels_29 = 11
Byte:01EB:       FastTraceLevels_30 = 11
Byte:01EC:       FastTraceLevels_31 = 10

           UDSTriggerControl Mode Page
              UDSTrigger Group
Byte:01F4:       Revision = 00
Byte:01F8:       TriggerControlFlags1_0 = 00
Byte:01FC:       TriggerCaptureFlags1_0 = 00
Byte:01FD:       TriggerCaptureFlags2_0 = 00
Byte:01FE:       SpecialTriggerCaptureFlags_0 = 00
Byte:0200:       TriggerResponseFlags1_0 = 00
Byte:0201:       TriggerResponseFlags2_0 = 00
Byte:0202:       SpecialTriggerResponseFlags_0 = 00
Byte:0204:       TriggerControlFlags1_1 = 00
Byte:0208:       TriggerCaptureFlags1_1 = 00
Byte:0209:       TriggerCaptureFlags2_1 = 00
Byte:020A:       SpecialTriggerCaptureFlags_1 = 00
Byte:020C:       TriggerResponseFlags1_1 = 00
Byte:020D:       TriggerResponseFlags2_1 = 00
Byte:020E:       SpecialTriggerResponseFlags_1 = 00
Byte:0210:       TriggerControlFlags1_2 = 80
Byte:0214:       TriggerCaptureFlags1_2 = F2
Byte:0215:       TriggerCaptureFlags2_2 = E8
```

```
Byte:0216:       SpecialTriggerCaptureFlags_2 = 80
Byte:0218:       TriggerResponseFlags1_2 = 00
Byte:0219:       TriggerResponseFlags2_2 = A0
Byte:021A:       SpecialTriggerResponseFlags_2 = 80
Byte:021C:       TriggerControlFlags1_3 = 80
Byte:0220:       TriggerCaptureFlags1_3 = F2
Byte:0221:       TriggerCaptureFlags2_3 = E8
Byte:0222:       SpecialTriggerCaptureFlags_3 = 80
Byte:0224:       TriggerResponseFlags1_3 = 00
Byte:0225:       TriggerResponseFlags2_3 = A0
Byte:0226:       SpecialTriggerResponseFlags_3 = 80
Byte:0228:       TriggerControlFlags1_4 = 80
Byte:022C:       TriggerCaptureFlags1_4 = F2
Byte:022D:       TriggerCaptureFlags2_4 = E8
Byte:022E:       SpecialTriggerCaptureFlags_4 = 80
Byte:0230:       TriggerResponseFlags1_4 = 02
Byte:0231:       TriggerResponseFlags2_4 = A0
Byte:0232:       SpecialTriggerResponseFlags_4 = 80
Byte:0234:       TriggerControlFlags1_5 = 80
Byte:0238:       TriggerCaptureFlags1_5 = F2
Byte:0239:       TriggerCaptureFlags2_5 = E8
Byte:023A:       SpecialTriggerCaptureFlags_5 = 80
Byte:023C:       TriggerResponseFlags1_5 = 00
Byte:023D:       TriggerResponseFlags2_5 = A0
Byte:023E:       SpecialTriggerResponseFlags_5 = 80
Byte:0240:       TriggerControlFlags1_6 = 80
Byte:0244:       TriggerCaptureFlags1_6 = 71
Byte:0245:       TriggerCaptureFlags2_6 = 00
Byte:0246:       SpecialTriggerCaptureFlags_6 = 80
Byte:0248:       TriggerResponseFlags1_6 = 00
Byte:0249:       TriggerResponseFlags2_6 = A0
Byte:024A:       SpecialTriggerResponseFlags_6 = 80
Byte:024C:       TriggerControlFlags1_7 = 80
Byte:0250:       TriggerCaptureFlags1_7 = 71
Byte:0251:       TriggerCaptureFlags2_7 = C8
Byte:0252:       SpecialTriggerCaptureFlags_7 = 80
Byte:0254:       TriggerResponseFlags1_7 = 00
Byte:0255:       TriggerResponseFlags2_7 = A0
Byte:0256:       SpecialTriggerResponseFlags_7 = 80
Byte:0258:       TriggerControlFlags1_8 = 80
Byte:025C:       TriggerCaptureFlags1_8 = F2
Byte:025D:       TriggerCaptureFlags2_8 = E8
Byte:025E:       SpecialTriggerCaptureFlags_8 = 80
Byte:0260:       TriggerResponseFlags1_8 = 00
Byte:0261:       TriggerResponseFlags2_8 = A0
Byte:0262:       SpecialTriggerResponseFlags_8 = 80
Byte:0264:       TriggerControlFlags1_9 = 80
Byte:0268:       TriggerCaptureFlags1_9 = F2
Byte:0269:       TriggerCaptureFlags2_9 = E8
Byte:026A:       SpecialTriggerCaptureFlags_9 = 80
Byte:026C:       TriggerResponseFlags1_9 = 00
Byte:026D:       TriggerResponseFlags2_9 = A0
Byte:026E:       SpecialTriggerResponseFlags_9 = 80
Byte:0270:       TriggerControlFlags1_10 = 00
Byte:0274:       TriggerCaptureFlags1_10 = 11
Byte:0275:       TriggerCaptureFlags2_10 = C8
Byte:0276:       SpecialTriggerCaptureFlags_10 = 80
```

```
Byte:0278:       TriggerResponseFlags1_10 = 00
Byte:0279:       TriggerResponseFlags2_10 = A0
Byte:027A:       SpecialTriggerResponseFlags_10 = 80
Byte:027C:       TriggerControlFlags1_11 = 80
Byte:0280:       TriggerCaptureFlags1_11 = F2
Byte:0281:       TriggerCaptureFlags2_11 = E8
Byte:0282:       SpecialTriggerCaptureFlags_11 = 80
Byte:0284:       TriggerResponseFlags1_11 = 00
Byte:0285:       TriggerResponseFlags2_11 = A0
Byte:0286:       SpecialTriggerResponseFlags_11 = 80
Byte:0288:       TriggerControlFlags1_12 = 80
Byte:028C:       TriggerCaptureFlags1_12 = F2
Byte:028D:       TriggerCaptureFlags2_12 = E8
Byte:028E:       SpecialTriggerCaptureFlags_12 = 80
Byte:0290:       TriggerResponseFlags1_12 = 00
Byte:0291:       TriggerResponseFlags2_12 = A0
Byte:0292:       SpecialTriggerResponseFlags_12 = 80
Byte:0294:       TriggerControlFlags1_13 = 00
Byte:0298:       TriggerCaptureFlags1_13 = 00
Byte:0299:       TriggerCaptureFlags2_13 = 00
Byte:029A:       SpecialTriggerCaptureFlags_13 = 00
Byte:029C:       TriggerResponseFlags1_13 = 00
Byte:029D:       TriggerResponseFlags2_13 = 00
Byte:029E:       SpecialTriggerResponseFlags_13 = 00
Byte:02A0:       TriggerControlFlags1_14 = 80
Byte:02A4:       TriggerCaptureFlags1_14 = F2
Byte:02A5:       TriggerCaptureFlags2_14 = E8
Byte:02A6:       SpecialTriggerCaptureFlags_14 = 80
Byte:02A8:       TriggerResponseFlags1_14 = 00
Byte:02A9:       TriggerResponseFlags2_14 = A0
Byte:02AA:       SpecialTriggerResponseFlags_14 = 80
Byte:02AC:       TriggerControlFlags1_15 = 80
Byte:02B0:       TriggerCaptureFlags1_15 = 71
Byte:02B1:       TriggerCaptureFlags2_15 = C8
Byte:02B2:       SpecialTriggerCaptureFlags_15 = 80
Byte:02B4:       TriggerResponseFlags1_15 = 00
Byte:02B5:       TriggerResponseFlags2_15 = A0
Byte:02B6:       SpecialTriggerResponseFlags_15 = 80
Byte:02B8:       TriggerControlFlags1_16 = 00
Byte:02BC:       TriggerCaptureFlags1_16 = F2
Byte:02BD:       TriggerCaptureFlags2_16 = C8
Byte:02BE:       SpecialTriggerCaptureFlags_16 = 80
Byte:02C0:       TriggerResponseFlags1_16 = 00
Byte:02C1:       TriggerResponseFlags2_16 = A0
Byte:02C2:       SpecialTriggerResponseFlags_16 = 80
Byte:02C4:       TriggerControlFlags1_17 = 00
Byte:02C8:       TriggerCaptureFlags1_17 = 00
Byte:02C9:       TriggerCaptureFlags2_17 = 00
Byte:02CA:       SpecialTriggerCaptureFlags_17 = 00
Byte:02CC:       TriggerResponseFlags1_17 = 00
Byte:02CD:       TriggerResponseFlags2_17 = 00
Byte:02CE:       SpecialTriggerResponseFlags_17 = 00
Byte:02D0:       TriggerControlFlags1_18 = 80
Byte:02D4:       TriggerCaptureFlags1_18 = 71
Byte:02D5:       TriggerCaptureFlags2_18 = C8
Byte:02D6:       SpecialTriggerCaptureFlags_18 = 80
Byte:02D8:       TriggerResponseFlags1_18 = 00
```

```
Byte:02D9:       TriggerResponseFlags2_18 = A0
Byte:02DA:       SpecialTriggerResponseFlags_18 = 80
Byte:02DC:       TriggerControlFlags1_19 = 00
Byte:02E0:       TriggerCaptureFlags1_19 = 00
Byte:02E1:       TriggerCaptureFlags2_19 = 00
Byte:02E2:       SpecialTriggerCaptureFlags_19 = 00
Byte:02E4:       TriggerResponseFlags1_19 = 00
Byte:02E5:       TriggerResponseFlags2_19 = 00
Byte:02E6:       SpecialTriggerResponseFlags_19 = 00
Byte:02E8:       TriggerControlFlags1_20 = 00
Byte:02EC:       TriggerCaptureFlags1_20 = 00
Byte:02ED:       TriggerCaptureFlags2_20 = 00
Byte:02EE:       SpecialTriggerCaptureFlags_20 = 00
Byte:02F0:       TriggerResponseFlags1_20 = 00
Byte:02F1:       TriggerResponseFlags2_20 = 00
Byte:02F2:       SpecialTriggerResponseFlags_20 = 00
Byte:02F4:       TriggerControlFlags1_21 = 00
Byte:02F8:       TriggerCaptureFlags1_21 = 00
Byte:02F9:       TriggerCaptureFlags2_21 = 00
Byte:02FA:       SpecialTriggerCaptureFlags_21 = 00
Byte:02FC:       TriggerResponseFlags1_21 = 00
Byte:02FD:       TriggerResponseFlags2_21 = 00
Byte:02FE:       SpecialTriggerResponseFlags_21 = 00

          UDSTraceBufferConfig Mode Page
             UDSBuffer Group
Byte:0304:       Revision = 00
Byte:0308:       MinimumAllocationInTraceBlocksMSB_0 = 00
Byte:0309:       MinimumAllocationInTraceBlocksLSB_0 = 05
Byte:030A:       TraceBufferControlFlags1_0 = 80
Byte:030C:       MinimumAllocationInTraceBlocksMSB_1 = 00
Byte:030D:       MinimumAllocationInTraceBlocksLSB_1 = 00
Byte:030E:       TraceBufferControlFlags1_1 = 00
Byte:0310:       MinimumAllocationInTraceBlocksMSB_2 = 00
Byte:0311:       MinimumAllocationInTraceBlocksLSB_2 = 00
Byte:0312:       TraceBufferControlFlags1_2 = 00
Byte:0314:       MinimumAllocationInTraceBlocksMSB_3 = 00
Byte:0315:       MinimumAllocationInTraceBlocksLSB_3 = 00
Byte:0316:       TraceBufferControlFlags1_3 = 00
Byte:0318:       MinimumAllocationInTraceBlocksMSB_4 = 00
Byte:0319:       MinimumAllocationInTraceBlocksLSB_4 = 00
Byte:031A:       TraceBufferControlFlags1_4 = 00
Byte:031C:       MinimumAllocationInTraceBlocksMSB_5 = 00
Byte:031D:       MinimumAllocationInTraceBlocksLSB_5 = 00
Byte:031E:       TraceBufferControlFlags1_5 = 00
Byte:0320:       MinimumAllocationInTraceBlocksMSB_6 = 00
Byte:0321:       MinimumAllocationInTraceBlocksLSB_6 = 00
Byte:0322:       TraceBufferControlFlags1_6 = 00
Byte:0324:       MinimumAllocationInTraceBlocksMSB_7 = 00
Byte:0325:       MinimumAllocationInTraceBlocksLSB_7 = 00
Byte:0326:       TraceBufferControlFlags1_7 = 00
Byte:0328:       MinimumAllocationInTraceBlocksMSB_8 = 00
Byte:0329:       MinimumAllocationInTraceBlocksLSB_8 = 00
Byte:032A:       TraceBufferControlFlags1_8 = 00
Byte:032C:       MinimumAllocationInTraceBlocksMSB_9 = 00
Byte:032D:       MinimumAllocationInTraceBlocksLSB_9 = 00
Byte:032E:       TraceBufferControlFlags1_9 = 00
```

```
Byte:0330:       MinimumAllocationInTraceBlocksMSB_10 = 00
Byte:0331:       MinimumAllocationInTraceBlocksLSB_10 = 00
Byte:0332:       TraceBufferControlFlags1_10 = 00
Byte:0334:       MinimumAllocationInTraceBlocksMSB_11 = 00
Byte:0335:       MinimumAllocationInTraceBlocksLSB_11 = 00
Byte:0336:       TraceBufferControlFlags1_11 = 00
Byte:0338:       MinimumAllocationInTraceBlocksMSB_12 = 00
Byte:0339:       MinimumAllocationInTraceBlocksLSB_12 = 00
Byte:033A:       TraceBufferControlFlags1_12 = 00
Byte:033C:       MinimumAllocationInTraceBlocksMSB_13 = 00
Byte:033D:       MinimumAllocationInTraceBlocksLSB_13 = 00
Byte:033E:       TraceBufferControlFlags1_13 = 00
Byte:0340:       MinimumAllocationInTraceBlocksMSB_14 = 00
Byte:0341:       MinimumAllocationInTraceBlocksLSB_14 = 00
Byte:0342:       TraceBufferControlFlags1_14 = 00
Byte:0344:       MinimumAllocationInTraceBlocksMSB_15 = 00
Byte:0345:       MinimumAllocationInTraceBlocksLSB_15 = 00
Byte:0346:       TraceBufferControlFlags1_15 = 00
Byte:0348:       MinimumAllocationInTraceBlocksMSB_16 = 00
Byte:0349:       MinimumAllocationInTraceBlocksLSB_16 = 00
Byte:034A:       TraceBufferControlFlags1_16 = 00
Byte:034C:       MinimumAllocationInTraceBlocksMSB_17 = 00
Byte:034D:       MinimumAllocationInTraceBlocksLSB_17 = 00
Byte:034E:       TraceBufferControlFlags1_17 = 00
Byte:0350:       MinimumAllocationInTraceBlocksMSB_18 = 00
Byte:0351:       MinimumAllocationInTraceBlocksLSB_18 = 00
Byte:0352:       TraceBufferControlFlags1_18 = 00
Byte:0354:       MinimumAllocationInTraceBlocksMSB_19 = 00
Byte:0355:       MinimumAllocationInTraceBlocksLSB_19 = 00
Byte:0356:       TraceBufferControlFlags1_19 = 00
Byte:0358:       MinimumAllocationInTraceBlocksMSB_20 = 00
Byte:0359:       MinimumAllocationInTraceBlocksLSB_20 = 00
Byte:035A:       TraceBufferControlFlags1_20 = 00
Byte:035C:       MinimumAllocationInTraceBlocksMSB_21 = 00
Byte:035D:       MinimumAllocationInTraceBlocksLSB_21 = 00
Byte:035E:       TraceBufferControlFlags1_21 = 00
Byte:0360:       MinimumAllocationInTraceBlocksMSB_22 = 00
Byte:0361:       MinimumAllocationInTraceBlocksLSB_22 = 00
Byte:0362:       TraceBufferControlFlags1_22 = 00
Byte:0364:       MinimumAllocationInTraceBlocksMSB_23 = 00
Byte:0365:       MinimumAllocationInTraceBlocksLSB_23 = 00
Byte:0366:       TraceBufferControlFlags1_23 = 00
Byte:0368:       MinimumAllocationInTraceBlocksMSB_24 = 00
Byte:0369:       MinimumAllocationInTraceBlocksLSB_24 = 00
Byte:036A:       TraceBufferControlFlags1_24 = 00
Byte:036C:       MinimumAllocationInTraceBlocksMSB_25 = 00
Byte:036D:       MinimumAllocationInTraceBlocksLSB_25 = 00
Byte:036E:       TraceBufferControlFlags1_25 = 00
Byte:0370:       MinimumAllocationInTraceBlocksMSB_26 = 00
Byte:0371:       MinimumAllocationInTraceBlocksLSB_26 = 00
Byte:0372:       TraceBufferControlFlags1_26 = 00
Byte:0374:       MinimumAllocationInTraceBlocksMSB_27 = 00
Byte:0375:       MinimumAllocationInTraceBlocksLSB_27 = 00
Byte:0376:       TraceBufferControlFlags1_27 = 00
Byte:0378:       MinimumAllocationInTraceBlocksMSB_28 = 00
Byte:0379:       MinimumAllocationInTraceBlocksLSB_28 = 00
Byte:037A:       TraceBufferControlFlags1_28 = 00
```

```
Byte:037C:       MinimumAllocationInTraceBlocksMSB_29 = 00
Byte:037D:       MinimumAllocationInTraceBlocksLSB_29 = 00
Byte:037E:       TraceBufferControlFlags1_29 = 00
Byte:0380:       MinimumAllocationInTraceBlocksMSB_30 = 00
Byte:0381:       MinimumAllocationInTraceBlocksLSB_30 = 00
Byte:0382:       TraceBufferControlFlags1_30 = 00
Byte:0384:       MinimumAllocationInTraceBlocksMSB_31 = 00
Byte:0385:       MinimumAllocationInTraceBlocksLSB_31 = 00
Byte:0386:       TraceBufferControlFlags1_31 = 00
Byte:0388:       MinimumAllocationInTraceBlocksMSB_32 = 00
Byte:0389:       MinimumAllocationInTraceBlocksLSB_32 = 00
Byte:038A:       TraceBufferControlFlags1_32 = 00
Byte:038C:       MinimumAllocationInTraceBlocksMSB_33 = 00
Byte:038D:       MinimumAllocationInTraceBlocksLSB_33 = 00
Byte:038E:       TraceBufferControlFlags1_33 = 00
Byte:0390:       MinimumAllocationInTraceBlocksMSB_34 = 00
Byte:0391:       MinimumAllocationInTraceBlocksLSB_34 = 00
Byte:0392:       TraceBufferControlFlags1_34 = 00
Byte:0394:       MinimumAllocationInTraceBlocksMSB_35 = 00
Byte:0395:       MinimumAllocationInTraceBlocksLSB_35 = 00
Byte:0396:       TraceBufferControlFlags1_35 = 00
Byte:0398:       MinimumAllocationInTraceBlocksMSB_36 = 00
Byte:0399:       MinimumAllocationInTraceBlocksLSB_36 = 00
Byte:039A:       TraceBufferControlFlags1_36 = 00
Byte:039C:       MinimumAllocationInTraceBlocksMSB_37 = 00
Byte:039D:       MinimumAllocationInTraceBlocksLSB_37 = 00
Byte:039E:       TraceBufferControlFlags1_37 = 00
Byte:03A0:       MinimumAllocationInTraceBlocksMSB_38 = 00
Byte:03A1:       MinimumAllocationInTraceBlocksLSB_38 = 00
Byte:03A2:       TraceBufferControlFlags1_38 = 00
Byte:03A4:       MinimumAllocationInTraceBlocksMSB_39 = 00
Byte:03A5:       MinimumAllocationInTraceBlocksLSB_39 = 00
Byte:03A6:       TraceBufferControlFlags1_39 = 00
Byte:03A8:       MinimumAllocationInTraceBlocksMSB_40 = 00
Byte:03A9:       MinimumAllocationInTraceBlocksLSB_40 = 00
Byte:03AA:       TraceBufferControlFlags1_40 = 00
Byte:03AC:       MinimumAllocationInTraceBlocksMSB_41 = 00
Byte:03AD:       MinimumAllocationInTraceBlocksLSB_41 = 00
Byte:03AE:       TraceBufferControlFlags1_41 = 00
Byte:03B0:       MinimumAllocationInTraceBlocksMSB_42 = 00
Byte:03B1:       MinimumAllocationInTraceBlocksLSB_42 = 00
Byte:03B2:       TraceBufferControlFlags1_42 = 00
Byte:03B4:       MinimumAllocationInTraceBlocksMSB_43 = 00
Byte:03B5:       MinimumAllocationInTraceBlocksLSB_43 = 00
Byte:03B6:       TraceBufferControlFlags1_43 = 00
Byte:03B8:       MinimumAllocationInTraceBlocksMSB_44 = 00
Byte:03B9:       MinimumAllocationInTraceBlocksLSB_44 = 00
Byte:03BA:       TraceBufferControlFlags1_44 = 00
Byte:03BC:       MinimumAllocationInTraceBlocksMSB_45 = 00
Byte:03BD:       MinimumAllocationInTraceBlocksLSB_45 = 00
Byte:03BE:       TraceBufferControlFlags1_45 = 00
Byte:03C0:       MinimumAllocationInTraceBlocksMSB_46 = 00
Byte:03C1:       MinimumAllocationInTraceBlocksLSB_46 = 00
Byte:03C2:       TraceBufferControlFlags1_46 = 00
Byte:03C4:       MinimumAllocationInTraceBlocksMSB_47 = 00
Byte:03C5:       MinimumAllocationInTraceBlocksLSB_47 = 00
Byte:03C6:       TraceBufferControlFlags1_47 = 00
```

```
Byte:03C8:       MinimumAllocationInTraceBlocksMSB_48 = 00
Byte:03C9:       MinimumAllocationInTraceBlocksLSB_48 = 00
Byte:03CA:       TraceBufferControlFlags1_48 = 00
Byte:03CC:       MinimumAllocationInTraceBlocksMSB_49 = 00
Byte:03CD:       MinimumAllocationInTraceBlocksLSB_49 = 00
Byte:03CE:       TraceBufferControlFlags1_49 = 00
Byte:03D0:       MinimumAllocationInTraceBlocksMSB_50 = 00
Byte:03D1:       MinimumAllocationInTraceBlocksLSB_50 = 00
Byte:03D2:       TraceBufferControlFlags1_50 = 00
Byte:03D4:       MinimumAllocationInTraceBlocksMSB_51 = 00
Byte:03D5:       MinimumAllocationInTraceBlocksLSB_51 = 00
Byte:03D6:       TraceBufferControlFlags1_51 = 00
Byte:03D8:       MinimumAllocationInTraceBlocksMSB_52 = 00
Byte:03D9:       MinimumAllocationInTraceBlocksLSB_52 = 00
Byte:03DA:       TraceBufferControlFlags1_52 = 00
Byte:03DC:       MinimumAllocationInTraceBlocksMSB_53 = 00
Byte:03DD:       MinimumAllocationInTraceBlocksLSB_53 = 00
Byte:03DE:       TraceBufferControlFlags1_53 = 00
Byte:03E0:       MinimumAllocationInTraceBlocksMSB_54 = 00
Byte:03E1:       MinimumAllocationInTraceBlocksLSB_54 = 00
Byte:03E2:       TraceBufferControlFlags1_54 = 00
Byte:03E4:       MinimumAllocationInTraceBlocksMSB_55 = 00
Byte:03E5:       MinimumAllocationInTraceBlocksLSB_55 = 00
Byte:03E6:       TraceBufferControlFlags1_55 = 00
Byte:03E8:       MinimumAllocationInTraceBlocksMSB_56 = 00
Byte:03E9:       MinimumAllocationInTraceBlocksLSB_56 = 00
Byte:03EA:       TraceBufferControlFlags1_56 = 00
Byte:03EC:       MinimumAllocationInTraceBlocksMSB_57 = 00
Byte:03ED:       MinimumAllocationInTraceBlocksLSB_57 = 00
Byte:03EE:       TraceBufferControlFlags1_57 = 00
Byte:03F0:       MinimumAllocationInTraceBlocksMSB_58 = 00
Byte:03F1:       MinimumAllocationInTraceBlocksLSB_58 = 00
Byte:03F2:       TraceBufferControlFlags1_58 = 00
Byte:03F4:       MinimumAllocationInTraceBlocksMSB_59 = 00
Byte:03F5:       MinimumAllocationInTraceBlocksLSB_59 = 00
Byte:03F6:       TraceBufferControlFlags1_59 = 00
Byte:03F8:       MinimumAllocationInTraceBlocksMSB_60 = 00
Byte:03F9:       MinimumAllocationInTraceBlocksLSB_60 = 00
Byte:03FA:       TraceBufferControlFlags1_60 = 00
Byte:03FC:       MinimumAllocationInTraceBlocksMSB_61 = 00
Byte:03FD:       MinimumAllocationInTraceBlocksLSB_61 = 00
Byte:03FE:       TraceBufferControlFlags1_61 = 00
Byte:0400:       MinimumAllocationInTraceBlocksMSB_62 = 00
Byte:0401:       MinimumAllocationInTraceBlocksLSB_62 = 00
Byte:0402:       TraceBufferControlFlags1_62 = 00
Byte:0404:       MinimumAllocationInTraceBlocksMSB_63 = 00
Byte:0405:       MinimumAllocationInTraceBlocksLSB_63 = 00
Byte:0406:       TraceBufferControlFlags1_63 = 00
Byte:0408:       MinimumAllocationInTraceBlocksMSB_64 = 00
Byte:0409:       MinimumAllocationInTraceBlocksLSB_64 = 00
Byte:040A:       TraceBufferControlFlags1_64 = 00
Byte:040C:       MinimumAllocationInTraceBlocksMSB_65 = 00
Byte:040D:       MinimumAllocationInTraceBlocksLSB_65 = 00
Byte:040E:       TraceBufferControlFlags1_65 = 00
Byte:0410:       MinimumAllocationInTraceBlocksMSB_66 = 00
Byte:0411:       MinimumAllocationInTraceBlocksLSB_66 = 00
Byte:0412:       TraceBufferControlFlags1_66 = 00
```

```
Byte:0414:       MinimumAllocationInTraceBlocksMSB_67 = 00
Byte:0415:       MinimumAllocationInTraceBlocksLSB_67 = 00
Byte:0416:       TraceBufferControlFlags1_67 = 00
Byte:0418:       MinimumAllocationInTraceBlocksMSB_68 = 00
Byte:0419:       MinimumAllocationInTraceBlocksLSB_68 = 00
Byte:041A:       TraceBufferControlFlags1_68 = 00
Byte:041C:       MinimumAllocationInTraceBlocksMSB_69 = 00
Byte:041D:       MinimumAllocationInTraceBlocksLSB_69 = 00
Byte:041E:       TraceBufferControlFlags1_69 = 00
Byte:0420:       MinimumAllocationInTraceBlocksMSB_70 = 00
Byte:0421:       MinimumAllocationInTraceBlocksLSB_70 = 00
Byte:0422:       TraceBufferControlFlags1_70 = 00
Byte:0424:       MinimumAllocationInTraceBlocksMSB_71 = 00
Byte:0425:       MinimumAllocationInTraceBlocksLSB_71 = 00
Byte:0426:       TraceBufferControlFlags1_71 = 00
Byte:0428:       MinimumAllocationInTraceBlocksMSB_72 = 00
Byte:0429:       MinimumAllocationInTraceBlocksLSB_72 = 00
Byte:042A:       TraceBufferControlFlags1_72 = 00
Byte:042C:       MinimumAllocationInTraceBlocksMSB_73 = 00
Byte:042D:       MinimumAllocationInTraceBlocksLSB_73 = 00
Byte:042E:       TraceBufferControlFlags1_73 = 00
Byte:0430:       MinimumAllocationInTraceBlocksMSB_74 = 00
Byte:0431:       MinimumAllocationInTraceBlocksLSB_74 = 00
Byte:0432:       TraceBufferControlFlags1_74 = 00
Byte:0434:       MinimumAllocationInTraceBlocksMSB_75 = 00
Byte:0435:       MinimumAllocationInTraceBlocksLSB_75 = 00
Byte:0436:       TraceBufferControlFlags1_75 = 00
Byte:0438:       MinimumAllocationInTraceBlocksMSB_76 = 00
Byte:0439:       MinimumAllocationInTraceBlocksLSB_76 = 00
Byte:043A:       TraceBufferControlFlags1_76 = 00

          Congen Mode Page
             Congen Group
Byte:0440:       NumDefaultATCyls = FF 3F
Byte:0442:       NumDefaultATHeads = 10 00
Byte:0444:       NumDefaultATSectors = 3F 00
Byte:0446:       NumECCBytesForLongCmds = 04 00
Byte:0448:       MaximumMultipleSize = 10
Byte:0449:       LogicalSectorAlignment = 00
Byte:044A:       PIOModeTiming = 00 02
Byte:044C:       DMAModeTiming = 00 02
Byte:044E:       NumCurrentATCyls = FF 3F
Byte:0450:       NumCurrentATHeads = 10 00
Byte:0452:       NumCurrentATSectors = 3F 00
Byte:0454:       CurrentMultipleSize = 10 01
Byte:0456:       MultiwordDMAModesSupported = 07
Byte:0456:           Bit:0, MULTIWORD_DMA_MODE_0_SUPPORTED = 1
Byte:0456:           Bit:1, MULTIWORD_DMA_MODE_1_SUPPORTED = 1
Byte:0456:           Bit:2, MULTIWORD_DMA_MODE_2_SUPPORTED = 1
Byte:0457:       UltraDMAModesSupported = 7F
Byte:0457:           Bit:0, ULTRA_DMA_MODE0_SUPPORTED = 1
Byte:0457:           Bit:1, ULTRA_DMA_MODE1_AND_BELOW_SUPPORTED = 1
Byte:0457:           Bit:2, ULTRA_DMA_MODE2_AND_BELOW_SUPPORTED = 1
Byte:0457:           Bit:3, ULTRA_DMA_MODE3_AND_BELOW_SUPPORTED = 1
Byte:0457:           Bit:4, ULTRA_DMA_MODE4_AND_BELOW_SUPPORTED = 1
Byte:0457:           Bit:5, ULTRA_DMA_MODE5_AND_BELOW_SUPPORTED = 1
Byte:0457:           Bit:6, ULTRA_DMA_MODE6_AND_BELOW_SUPPORTED = 1
```

```
Byte:0458:       AdvancedPIOModesSupported = 03 00
Byte:0458:           Bit:0, PIO_MODE_3_SUPPORTED = 1
Byte:0458:           Bit:1, PIO_MODE_4_SUPPORTED = 1
Byte:045A:       MinimumMultiwordDMACycleTime = 78 00
Byte:045C:       RecommendedMultiwordDMACycleTime = 78 00
Byte:045E:       MinimumPIOTimeWithoutFlowControl = 78 00
Byte:0460:       MinimumPIOTimeWithFlowControl = 78 00
Byte:0462:       QDepth = 1F 00
Byte:0464:       SATACapabilities = 0E 05
Byte:0465:           Bit:0, SATA_NCQ_SUPPORTED = 1
Byte:0465:           Bit:1, HOST_INITIATED_PHY_POWER_MANAGEMENT_SUPPORTED = 0
Byte:0465:           Bit:2, SATA_PHY_EVENT_COUNTERS_SUPPORTED = 1
Byte:0465:           Bit:3, UNLOAD_WHILE_NCQ_CMDS_OUTSTANDING_SUPPORTED = 0
Byte:0465:           Bit:4, NCQ_PRIORITY_SUPPORTED = 0
Byte:0468:       SATAFeaturesSupported = 48 00
Byte:0468:           Bit:2, DMA_AUTOACTIVATE_SUPPORTED = 0
Byte:0468:           Bit:3, DEVICE_INITIATED_POWER_MANAGEMENT_SUPPORTED = 1
Byte:0468:           Bit:6, PRESERVE_SETTINGS_ON_COMRESET_SUPPORTED = 1
Byte:046A:       SATAFeaturesEnabled = 40 00
Byte:046A:           Bit:2, DMA_AUTOACTIVATE_ENABLED = 0
Byte:046A:           Bit:3, DEVICE_INITIATED_POWER_MANAGEMENT_ENABLED = 0
Byte:046A:           Bit:6, PRESERVE_SETTINGS_ON_COMRESET_ENABLED = 1
Byte:046C:       MajorVersionNumber = F0 01
Byte:046C:           Bit:1, ATAPI_1_SUPPORTED = 0
Byte:046C:           Bit:2, ATAPI_2_SUPPORTED = 0
Byte:046C:           Bit:3, ATAPI_3_SUPPORTED = 0
Byte:046C:           Bit:4, ATAPI_4_SUPPORTED = 1
Byte:046C:           Bit:5, ATAPI_5_SUPPORTED = 1
Byte:046C:           Bit:6, ATAPI_6_SUPPORTED = 1
Byte:046C:           Bit:7, ATAPI_7_SUPPORTED = 1
Byte:046D:           Bit:0, ATAPI_8_ACS_SUPPORTED = 1
Byte:046E:       MinorVersionNumber = 29 00
Byte:0470:       CommandSetSupported = 6B 34
Byte:0470:           Bit:0, SMART_SUPPORTED = 1
Byte:0470:           Bit:1, SECURITY_MODE_FEATURE_SET_SUPPORTED = 1
Byte:0470:           Bit:5, WRITE_CACHE_SUPPORTED = 1
Byte:0470:           Bit:6, READ_LOOKAHEAD_SUPPORTED = 1
Byte:0471:           Bit:2, HOST_PROTECTED_AREA_SUPPORTED = 1
Byte:0471:           Bit:4, WRITE_BUFFER_CMD_SUPPORTED = 1
Byte:0471:           Bit:5, READ_BUFFER_CMD_SUPPORTED = 1
Byte:0471:           Bit:6, NOP_CMD_SUPPORTED = 0
Byte:0472:       CommandSetsSupported = 09 7D
Byte:0472:           Bit:1, READ_WRITE_DMA_QUEUED_SUPPORTED = 0
Byte:0472:           Bit:3, ADVANCED_POWER_MANAGEMENT_SUPPORTED = 1
Byte:0472:           Bit:5, POWER_UP_IN_STANDBY_SUPPORTED = 0
Byte:0472:           Bit:6, SET_FEATURES_SPINUP_REQUIRED_SUPPORTED = 0
Byte:0473:           Bit:0, SET_MAX_SECURITY_EXTENSION_SUPPORTED = 1
Byte:0473:           Bit:1, AUTO_ACOUSTIC_MANAGEMENT_SUPPORTED = 0
Byte:0473:           Bit:2, FOURTYEIGHT_BIT_SUPPORTED = 1
Byte:0473:           Bit:3, DCO_COMMAND_SETS_SUPPORTED = 1
Byte:0473:           Bit:4, FLUSH_CACHE_SUPPORTED = 1
Byte:0473:           Bit:5, FLUSH_CACHE_EXT_SUPPORTED = 1
Byte:0474:       CommandSetSupportedExtension = 23 61
Byte:0474:           Bit:0, SMART_ERROR_LOGGING_SUPPORTED = 1
Byte:0474:           Bit:1, SMART_SELF_TEST_SUPPORTED = 1
Byte:0474:           Bit:2, MEDIA_SERIALNUMBER_SUPPORTED = 0
Byte:0474:           Bit:4, STREAMING_FEATURE_SET_SUPPORTED = 0
```

```
Byte:0474:           Bit:6, FUA_WRITE_EXT_COMMANDS_SUPPORTED = 0
Byte:0474:           Bit:7, FUA_WRITE_QUEUED_EXT_COMMANDS_SUPPORTED = 0
Byte:0475:           Bit:0, SIXTYFOUR_BIT_WWN_SUPPORTED = 1
Byte:0475:           Bit:3, AV_LITE_SUPPORTED = 0
Byte:0475:           Bit:4, AV_LITE_RC_WC_SUPPORTED = 0
Byte:0475:           Bit:5, IDLE_IMMEDIATE_UNLOAD_SUPPORTED = 1
Byte:0476:       CommandSetEnabled = 69 34
Byte:0476:           Bit:0, SMART_ENABLED = 1
Byte:0476:           Bit:1, SECURITY_MODE_FEATURE_SET_ENABLED = 0
Byte:0476:           Bit:5, WRITE_CACHE_ENABLED = 1
Byte:0476:           Bit:6, READ_LOOKAHEAD_ENABLED = 1
Byte:0477:           Bit:2, HOST_PROTECTED_AREA_ENABLED = 1
Byte:0478:       CommandSetsEnabled = 09 BC
Byte:0478:           Bit:3, ADVANCED_POWER_MANAGEMENT_ENABLED = 1
Byte:0478:           Bit:5, POWER_UP_IN_STANDBY_ENABLED = 0
Byte:0478:           Bit:6, SET_FEATURES_SPINUP_REQUIRED_ENABLED = 0
Byte:0478:           Bit:7, ADDRESS_OFFSET_RESERVED_AREA_BOOT = 0
Byte:0479:           Bit:0, SET_MAX_SECURITY_EXTENSION_ENABLED = 0
Byte:0479:           Bit:1, AUTO_ACOUSTIC_MANAGEMENT_ENABLED = 0
Byte:047A:       CommandSetEnabledExtension = 23 61
Byte:047A:           Bit:2, MEDIA_SERIALNUMBER_IS_VALID = 0
Byte:047B:           Bit:3, AV_LITE_ENABLED = 0
Byte:047B:           Bit:4, AV_LITE_RC_WC_ENABLED = 0
Byte:047C:       FreeFallSensitivity = 00 00
Byte:047E:       CurrentAPMValue = 80 80
Byte:0480:       MasterPasswordRevisionCode = FE FF
Byte:0482:       AcousticLevel = 00 FE
Byte:0484:       StreamMinRequestSize = 00 00
Byte:0486:       StreamXferTimeDMA = 00 00
Byte:0488:       StreamAccessLatency = 00 00
Byte:048A:       StreamPerformanceGranularity = 00 00 00 00
Byte:048E:       StreamXferTimePIO = 00 00
Byte:0490:       Max512ByteUnitsPerDownloadMicrocode = 00 00
Byte:0492:       PhysicalLogicalSectorSize = 00 00
Byte:0492:           Bit:0, TWO_X_LOG_SECTORS_PER_PHY_SECTOR_B0 = 0
Byte:0492:           Bit:1, TWO_X_LOG_SECTORS_PER_PHY_SECTOR_B1 = 0
Byte:0492:           Bit:2, TWO_X_LOG_SECTORS_PER_PHY_SECTOR_B2 = 0
Byte:0492:           Bit:3, TWO_X_LOG_SECTORS_PER_PHY_SECTOR_B3 = 0
Byte:0493:           Bit:4, DEV_LOG_SEC_LEN_GREATER_THAN_256W = 0
Byte:0493:           Bit:5, DEV_HAS_MUL_LOG_SECTORS_PER_PHY_SECTOR = 0
Byte:0494:       AVLiteWorstCaseTimer = 00 00
Byte:0496:       WordsPerLogicalSector = 00 01 00 00
Byte:049A:       ATACommandSetSupportedExt2 = 0E 40
Byte:049A:           Bit:1, WRITE_READ_VERIFY_SUPPORTED = 1
Byte:049A:           Bit:2, WRITE_UNCORRECTABLE_SUPPORTED = 1
Byte:049A:           Bit:3, READ_AND_WRITE_DMA_EXP_GPL_COMMANDS_SUPPORTED = 1
Byte:049A:           Bit:5, FREEFALL_SENSOR_SUPPORTED = 0
Byte:049C:       ATACommandSetEnabledExt2 = 08 40
Byte:049C:           Bit:1, WRITE_READ_VERIFY_ENABLED = 0
Byte:049C:           Bit:3, READ_AND_WRITE_DMA_EXP_GPL_COMMANDS_ENABLED = 1
Byte:049C:           Bit:5, FREEFALL_SENSOR_ENABLED_BY_DEFAULT = 0
Byte:049E:       ATATransportMajorRevisionNumber = 08 10
Byte:04A0:       ATATransportMinorRevisionNumber = 00 00
Byte:04A2:       ATSMARTDefaultFlags = 01 00
Byte:04A2:           Bit:0, SMARTDEFAULTON = 1
Byte:04A4:       ATSMARTConfig =
                 0F 00 06 00 03 00 00 00 32 00 14 00 33 00 24 00
```

```
                0F 00 1E 00 32 00 00 00 13 00 61 00 32 00 14 00
                32 00 00 00 3A 00 00 00 22 00 2D 00 32 00 00 00
                32 00 00 00 32 00 00 00 22 00 00 00 1A 00 00 00
                12 00 00 00 10 00 00 00 3E 00 00 00 32 00 63 00
                32 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
                00 00 00 00 32 00 00 00 00 00 00 00 00 00 00 00
                00 00 00 00 00 00 00 00
Byte:04A4:      SMART1Status = 0F 00
Byte:04A6:      SMART1Threshold = 06
Byte:04A8:      SMART2Status = 03 00
Byte:04AA:      SMART2Threshold = 00
Byte:04AC:      SMART3Status = 32 00
Byte:04AE:      SMART3Threshold = 14
Byte:04B0:      SMART4Status = 33 00
Byte:04B2:      SMART4Threshold = 24
Byte:04B4:      SMART5Status = 0F 00
Byte:04B6:      SMART5Threshold = 1E
Byte:04B8:      SMART6Status = 32 00
Byte:04BA:      SMART6Threshold = 00
Byte:04BC:      SMART7Status = 13 00
Byte:04BE:      SMART7Threshold = 61
Byte:04C0:      SMART8Status = 32 00
Byte:04C2:      SMART8Threshold = 14
Byte:04C4:      SMART9Status = 32 00
Byte:04C6:      SMART9Threshold = 00
Byte:04C8:      SMART10Status = 3A 00
Byte:04CA:      SMART10Threshold = 00
Byte:04CC:      SMART11Status = 22 00
Byte:04CE:      SMART11Threshold = 2D
Byte:04D0:      SMART12Status = 32 00
Byte:04D2:      SMART12Threshold = 00
Byte:04D4:      SMART13Status = 32 00
Byte:04D6:      SMART13Threshold = 00
Byte:04D8:      SMART14Status = 32 00
Byte:04DA:      SMART14Threshold = 00
Byte:04DC:      SMART15Status = 22 00
Byte:04DE:      SMART15Threshold = 00
Byte:04E0:      SMART16Status = 1A 00
Byte:04E2:      SMART16Threshold = 00
Byte:04E4:      SMART17Status = 12 00
Byte:04E6:      SMART17Threshold = 00
Byte:04E8:      SMART18Status = 10 00
Byte:04EA:      SMART18Threshold = 00
Byte:04EC:      SMART19Status = 3E 00
Byte:04EE:      SMART19Threshold = 00
Byte:04F0:      SMART20Status = 32 00
Byte:04F2:      SMART20Threshold = 63
Byte:04F4:      SMART21Status = 32 00
Byte:04F6:      SMART21Threshold = 00
Byte:04F8:      SMART22Status = 00 00
Byte:04FA:      SMART22Threshold = 00
Byte:04FC:      SMART23Status = 00 00
Byte:04FE:      SMART23Threshold = 00
Byte:0500:      SMART24Status = 00 00
Byte:0502:      SMART24Threshold = 00
Byte:0504:      SMART25Status = 00 00
Byte:0506:      SMART25Threshold = 00
```

```
Byte:0508:       SMART26Status = 32 00
Byte:050A:       SMART26Threshold = 00
Byte:050C:       SMART27Status = 00 00
Byte:050E:       SMART27Threshold = 00
Byte:0510:       SMART28Status = 00 00
Byte:0512:       SMART28Threshold = 00
Byte:0514:       SMART29Status = 00 00
Byte:0516:       SMART29Threshold = 00
Byte:0518:       SMART30Status = 00 00
Byte:051A:       SMART30Threshold = 00
Byte:051E:       SCTCommandSetSupported = 3B 10
Byte:051E:           Bit:0, SCT_SUPPORTED_COMMAND_SET = 1
Byte:051E:           Bit:1, SCT_SUPPORTED_LONG_SECTOR_ACCESS = 1
Byte:051E:           Bit:2, SCT_SUPPORTED_LBA_SEGMENT_ACCESS = 0
Byte:051E:           Bit:3, SCT_SUPPORTED_ERROR_RECOVERY_CONTROL = 1
Byte:051E:           Bit:4, SCT_SUPPORTED_FEATURES_CONTROL = 1
Byte:051E:           Bit:5, SCT_SUPPORTED_DATA_TABLE = 1
Byte:0520:       SCTFanControlMaxOperatingTemp = 00
Byte:0521:       SCTFanControlOverRangeTemp = 00
Byte:0522:       SCTFanControlUnderRangeTemp = 00
Byte:0523:       SCTFanControlMinOperatingTemp = 00
Byte:0524:       SCTVendorFlags = 04 00
Byte:0524:           Bit:0, SCT_VENDORFLAGS_SEATOOLS = 0
Byte:0524:           Bit:1, SCT_VENDORFLAGS_WRITE_SAME = 0
Byte:0524:           Bit:2, SCT_VENDORFLAGS_CORRECTABLE_BIT = 1
Byte:0525:           Bit:7, SCT_VENDORFLAGS_DEBUG_MODE = 0
Byte:0526:       SCTTimerReadDefault = 00 00
Byte:0528:       SCTTimerWriteDefault = 00 00
Byte:052A:       SCTTenMsecCount = 00
Byte:052B:       SCTPerformanceFlags = 00
Byte:052C:       SCTTempDataTableSize = 80 00
Byte:052E:       SCTSamplingPeriod = 01 00
Byte:0530:       SCTInterval = 01 00
Byte:0532:       ATAPadSlewRate = 00
Byte:0533:       ATAIORDYPadControl = 00
Byte:0534:       PreampHotTweak = 00
Byte:0535:       PreampColdTweak = 00
Byte:0536:       LubeMitigationRetries = 00 00
Byte:0538:       LengthOfWriteSpaceToFlush = 00 00
Byte:053A:       LengthOfWriteCommandToTriggerFlush = 00 00
Byte:053C:       NDSLBAThresholdA = 00 00
Byte:053E:       NDSLBAThresholdB = 00 00
Byte:0540:       NDSPartitionDependencies = 00 00
Byte:0542:       NDSODOffsetConfig = 00
Byte:0543:       NDSIDOffsetConfig = 00
Byte:0544:       APMTimer1mSec = FF 00
Byte:0546:       APMTimer2mSec = 28 23
Byte:0548:       APMStandByTimer = 20 00
Byte:054A:       APMTimerForDither = 2C 01
Byte:054C:       ReadDelayMinimum = 00
Byte:054D:       WriteDelayMinimum = 00
Byte:054E:       WriteDelayIncremental = 00
Byte:054F:       ReadDelayIncremental = 00
Byte:0550:       FeatureFlags = 80 A0 00 08
Byte:0550:           Bit:0, WRITE_CONFIG_DATA_TO_FLASH = 0
Byte:0550:           Bit:1, SPINUP_WRITE_FAULT_THRESHOLDS_ENABLED = 0
Byte:0550:           Bit:2, IOEDC_CHECK_ENABLED = 0
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Byte:0550:           Bit:3, IOEDC_ERROR_ENABLED = 0
Byte:0550:           Bit:4, DOWNLOAD_MICROCODE_FUTURE_USE_ONLY = 0
Byte:0550:           Bit:5, SUPPRESS_SERIAL_PORT_PRINTS = 0
Byte:0550:           Bit:6, DRQ_CLEAR_ON_PIO_READ_ERR_SUPPORTED = 0
Byte:0550:           Bit:7, OFFLINE_SEEK_AWAY = 1
Byte:0551:           Bit:0, DELAY_SLEEP_STANDBY_CMDCOMPLETE = 0
Byte:0551:           Bit:1, IDLE_IMMEDIATE_UNLOAD_EMERGENCY = 0
Byte:0551:           Bit:2, ENABLE_ALLOW_RAW_ERROR_RATE_UPDATE = 0
Byte:0551:           Bit:3, OP_SHOCK_DETECTION_ENABLED = 0
Byte:0551:           Bit:4, LOG99_CONTROL = 0
Byte:0551:           Bit:5, READ_WRITE_LONG_EXTENDED_ENABLED = 1
Byte:0551:           Bit:6, WRITE_REORDERING_DISABLED = 0
Byte:0551:           Bit:7, ALLOW_AAM_FEATURE_SET = 1
Byte:0552:           Bit:0, DATA_LOG_ENABLED = 0
Byte:0552:           Bit:1, HOST_STREAM_RECORD_FIRST_ERROR_BLOCK = 0
Byte:0552:           Bit:2, SAVE_ATA_COMMAND_HISTORY_TO_DISC = 0
Byte:0552:           Bit:3, EIB_ON_POWERUP = 0
Byte:0552:           Bit:4, LEFT_JUSTIFY_SERIAL_NUMBER = 0
Byte:0552:           Bit:5, SUN_MICRO_MODEL_NUMBER_UPDATE = 0
Byte:0552:           Bit:6, REVERTING_TO_POWER_ON_DEFAULTS_SUPPORTED = 0
Byte:0552:           Bit:7, RIGHT_JUSTIFY_FIRMWARE_REVISION = 0
Byte:0553:           Bit:0, SEGMENT_INITIALIZED = 0
Byte:0553:           Bit:1, DISPLAY_SMART_COMMANDS_ON_SERIAL_PORT_ENABLED = 0
Byte:0553:           Bit:2, DISABLE_SMART_AUTO_UPDATES = 0
Byte:0553:           Bit:3, DISABLE_LEGACY_READ_WRITE_LONG = 1
Byte:0553:           Bit:4, VERSION_ENFORCEMENT_SUPPORTED = 0
Byte:0553:           Bit:5, ENABLE_MICKEY_CERT = 0
Byte:0553:           Bit:6, RETAIN_AMPS_VALUE_ON_DOWNLOAD = 0
Byte:0554:       PerformanceFlags = 00 3F
Byte:0554:           Bit:0, REDUCED_RAW_TRANSITION_FLUSH = 0
Byte:0554:           Bit:1, REDUCED_AV_RETRIES = 0
Byte:0554:           Bit:4, VJIT_DISABLED = 0
Byte:0554:           Bit:5, ZERO_LATENCY_RD_ENABLED = 0
Byte:0555:           Bit:0, DAR_ENABLED = 1
Byte:0555:           Bit:1, OFFLINE_SPARING_ENABLED = 1
Byte:0555:           Bit:2, JIT3 = 1
Byte:0555:           Bit:3, JIT2 = 1
Byte:0555:           Bit:4, JIT1 = 1
Byte:0555:           Bit:5, JIT0 = 1
Byte:0555:           Bit:6, DISABLE_IDLE_ACTIVITY = 0
Byte:0555:           Bit:7, ENABLE_SELF_SEEK = 0
Byte:0556:       AggressivelyScanThisManyTimes = 00 00
Byte:0558:       LongSeekEventTime = FF FF
Byte:055A:       SleepStandbyDelay = 00 00
Byte:055C:       CustomerUniques = 00 00 00 00
Byte:0560:       CustomerType = 00 00 00 00
Byte:0564:       AdditionalSATAFeatureConfig = 0F 06 00 00
Byte:0564:           Bit:0, SATA_FORCE_EARLY_STATUS = 1
Byte:0564:           Bit:1, SATA_EARLY_STATUS_FORCE_COMRESET = 1
Byte:0564:           Bit:2, SATA_INTRACOMMAND_PHY_MANAGEMENT_ENABLED = 1
Byte:0564:           Bit:3, SATA_ENABLE_PHY_PM_CALIBRATION = 1
Byte:0564:           Bit:4, SATA_ENABLE_SPI_VIS_MODE = 0
Byte:0564:           Bit:5, SATA_ENABLE_RERR_TESTING = 0
Byte:0564:           Bit:6, SATA_ENABLE_SSC = 0
Byte:0564:           Bit:7, SATA_EYE_DIAGRAM_TEST = 0
Byte:0565:           Bit:0, SATA_DISABLE_PRIMITIVE_SCRAMBLING = 0
Byte:0565:           Bit:1, SATA_ULTRA_AGGRESSIVE_INTER_COMMAND_PHY_SUPPORTED = 1
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Byte:0565:           Bit:2, SATA_ULTRA_AGGRESSIVE_INTRA_COMMAND_PHY_SUPPORTED = 1
Byte:0568:       FactoryODScanMBytes = 00 00
Byte:056A:       FactoryIDScanMBytes = 00 00
Byte:056C:       MaxDSTSelfTestTime = 54 15
Byte:056E:       DSTShortTestTimeLimit = 3C 00
Byte:0570:       BootFlagsForROM = 02 00
Byte:0570:           Bit:0, BOOT_FLAG_POWER_ON_IN_STANDBY = 0
Byte:0570:           Bit:1, BOOT_FLAG_PARTIAL_OOB = 1
Byte:0570:           Bit:2, USE_SMALL_ID_FORMAT = 0
Byte:0570:           Bit:3, LOW_CURRENT_SPINUP = 0
Byte:0572:       LastCongenWriteCaller = 00 00
Byte:0574:       HostStreamJumpSizeInSectors = 00 00
Byte:0576:       HostStreamTotalJumpSteps = 00
Byte:0577:       SerialDebugLevel = 00
Byte:0578:       DefaultStandbyTimer = 00
Byte:0579:       ATAInterSectorTime = 08
Byte:057A:       StandbyDelayBeforePostingStatus = 00 00
Byte:057C:       StandbyPostingStatusFlags = 06 00
Byte:057C:           Bit:0,
ENABLE_WAIT_FOR_DRIVE_SPINDOWN_BEFORE_POSTING_STATUS_AFTER_STANDBY = 0
Byte:057C:           Bit:1, ENABLE_HEADS_ON_RAMP_BEFORE_POSTING_STATUS_AFTER_STANDBY = 1
Byte:057C:           Bit:2, ENABLE_DELAY_BEFORE_POSTING_STATUS_AFTER_STANDBY = 1
Byte:057F:       LTTCPowerOnHours = 0A
Byte:0580:       DebugAddress0 = 00 00 00 00
Byte:0584:       DebugValue0 = 00 00 00 00
Byte:0588:       DebugAddress1 = 00 00 00 00
Byte:058C:       DebugValue1 = 00 00 00 00
Byte:0590:       MaxDSTUnrecoverableFailures = 00 00
Byte:0592:           Bit:0, EIB_WRT_MSG_ENABLE = 0
Byte:0592:           Bit:1, EIB_SWD_MSG_ENABLE = 0
Byte:0592:           Bit:2, EIB_SIM_MSG_ENABLE = 0
Byte:0592:           Bit:3, EIB_DEBUG_MSG_ENABLE = 0
Byte:0593:       Expansion =
                 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
                 00 00 00 00 00 00 00 00 00 00 00 00 00 00
                 ''
```