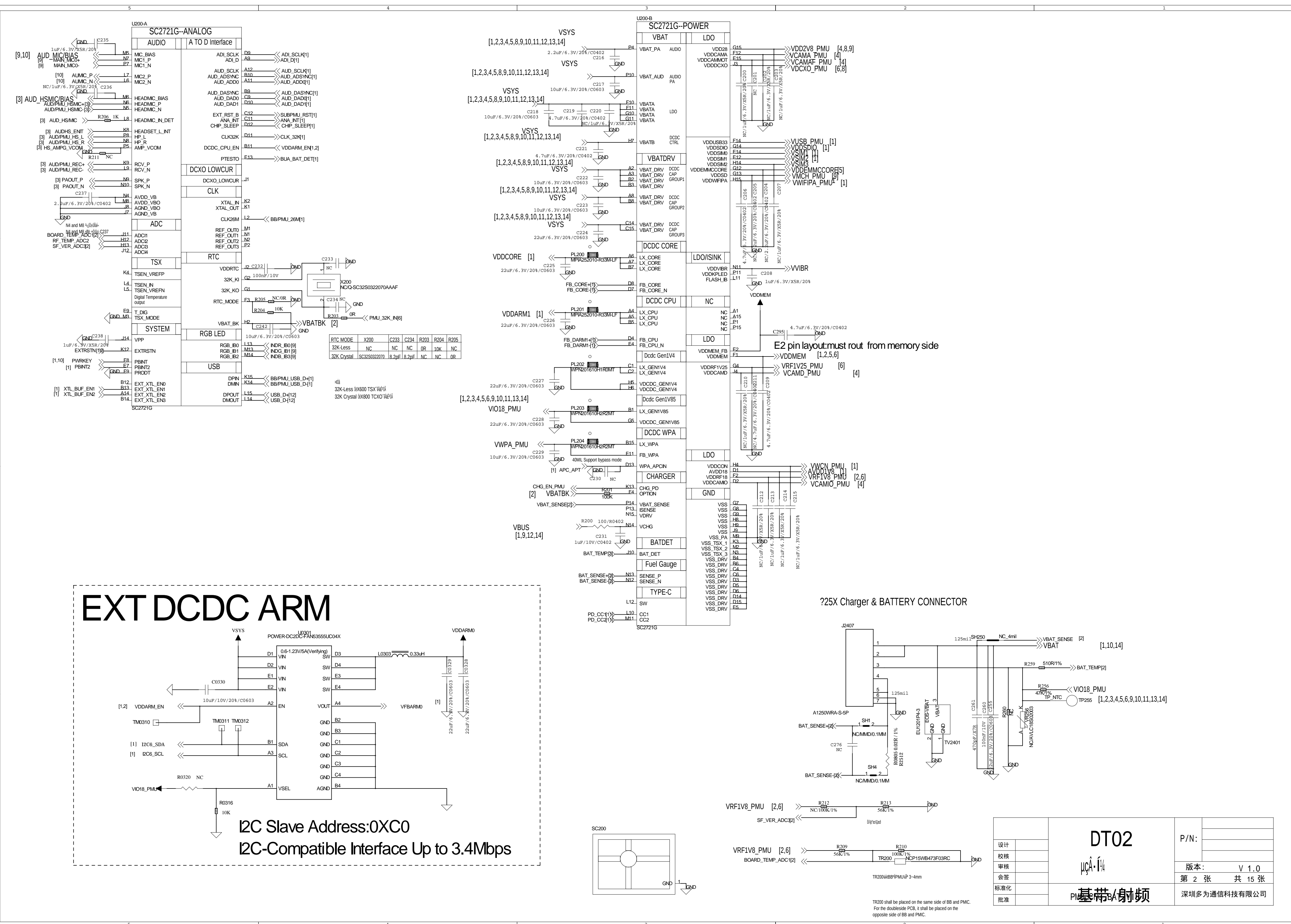
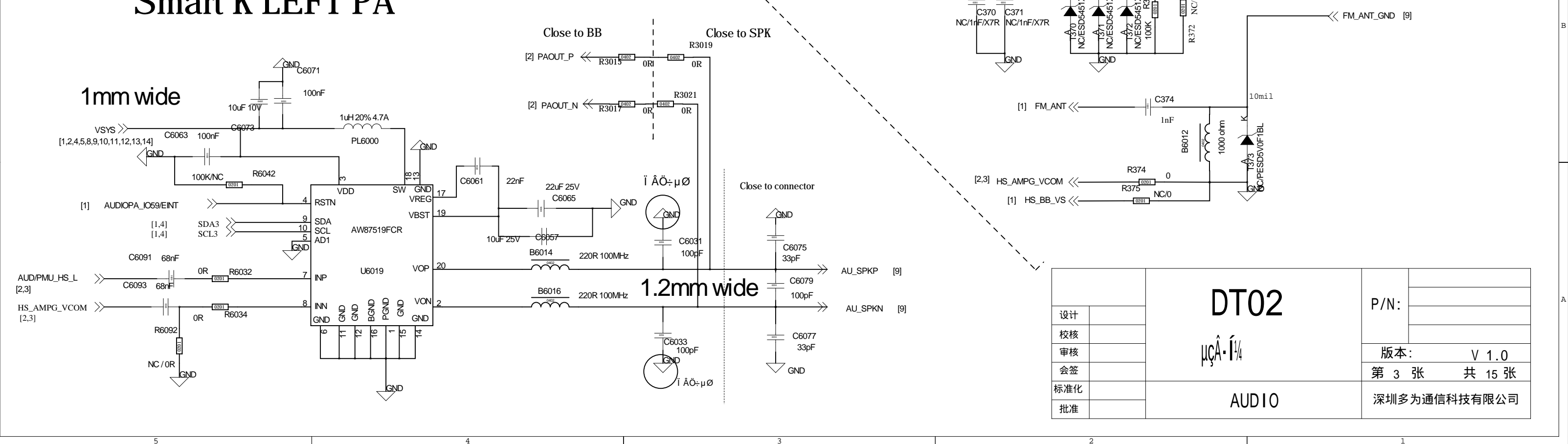
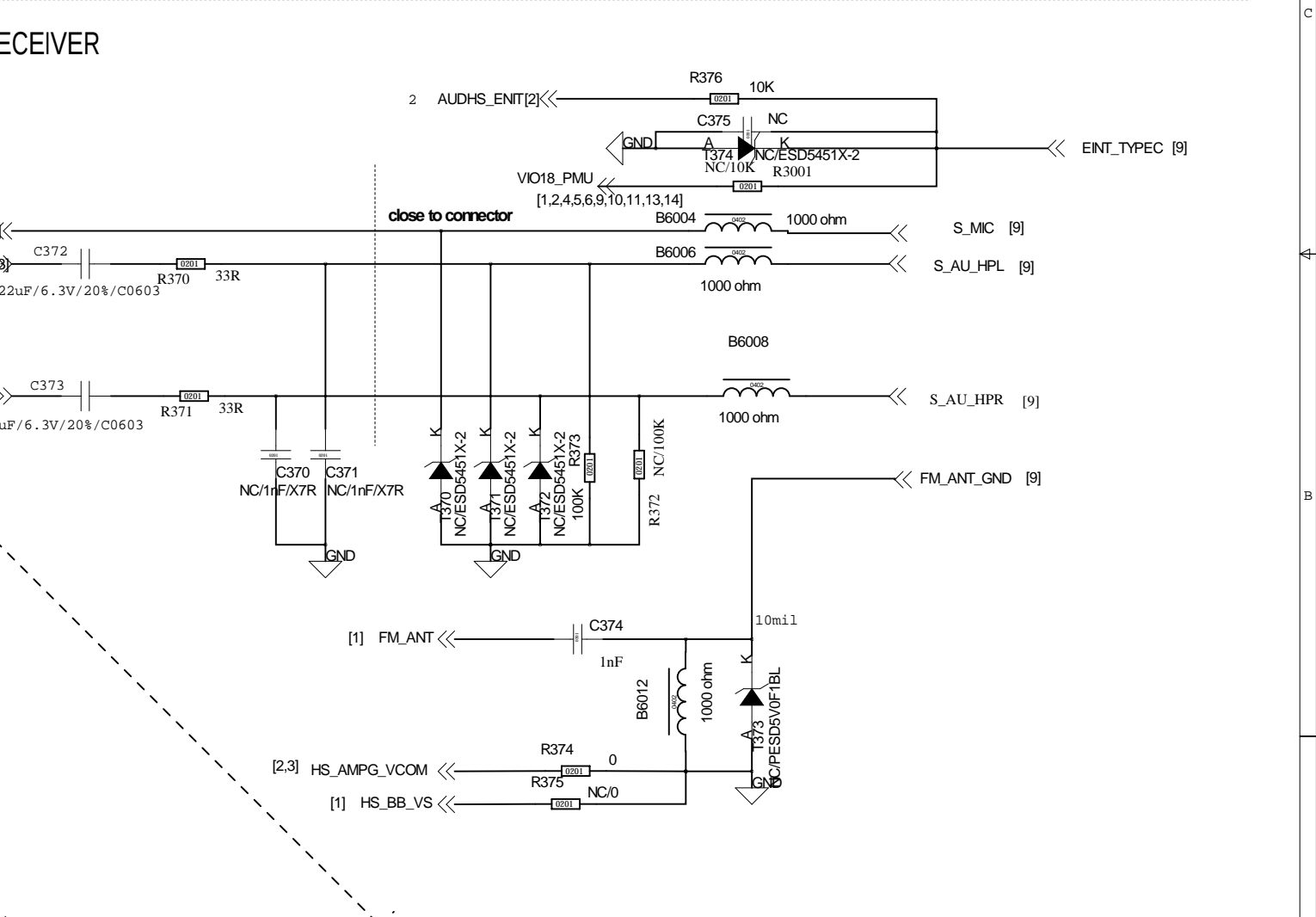
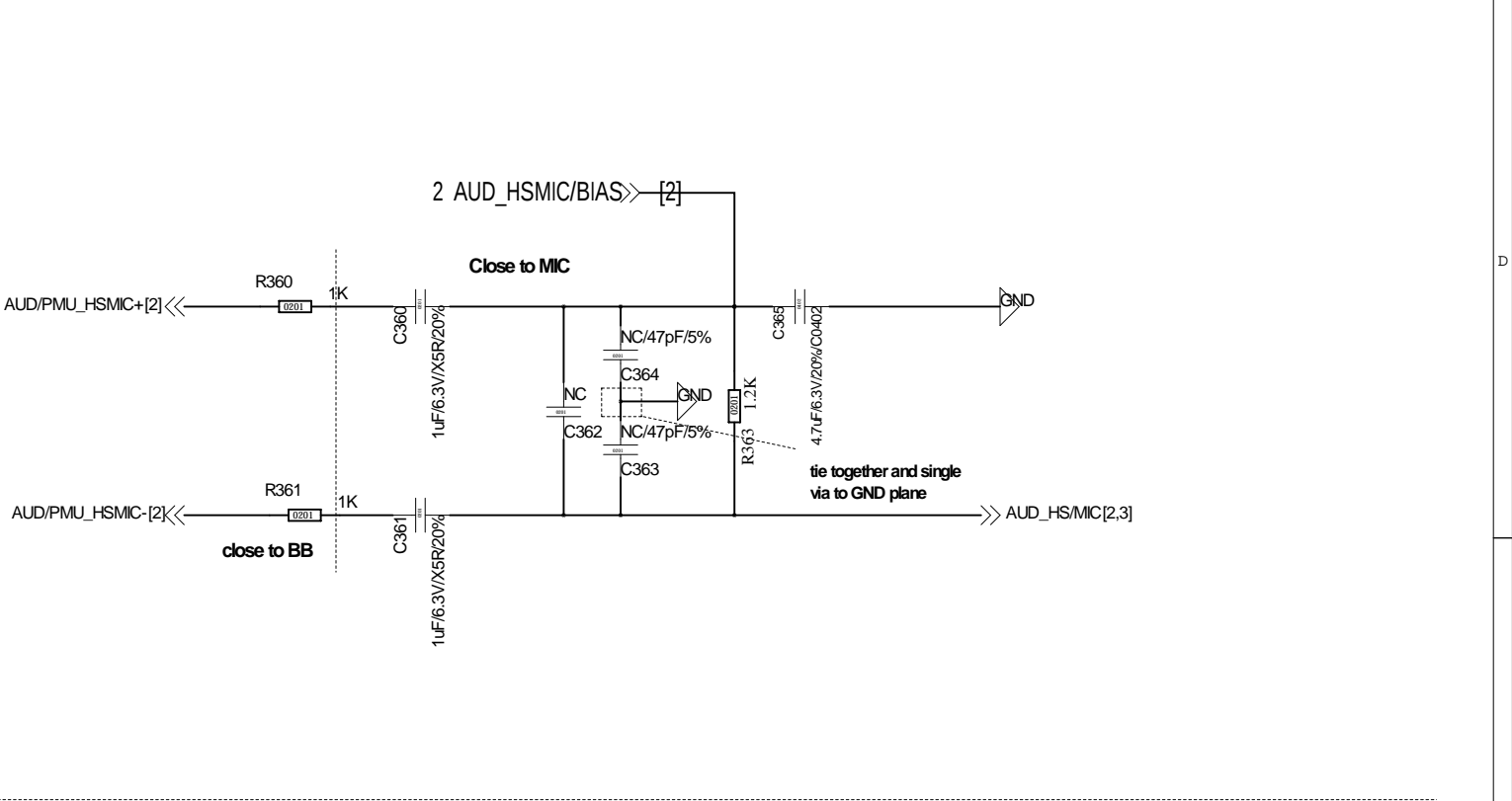
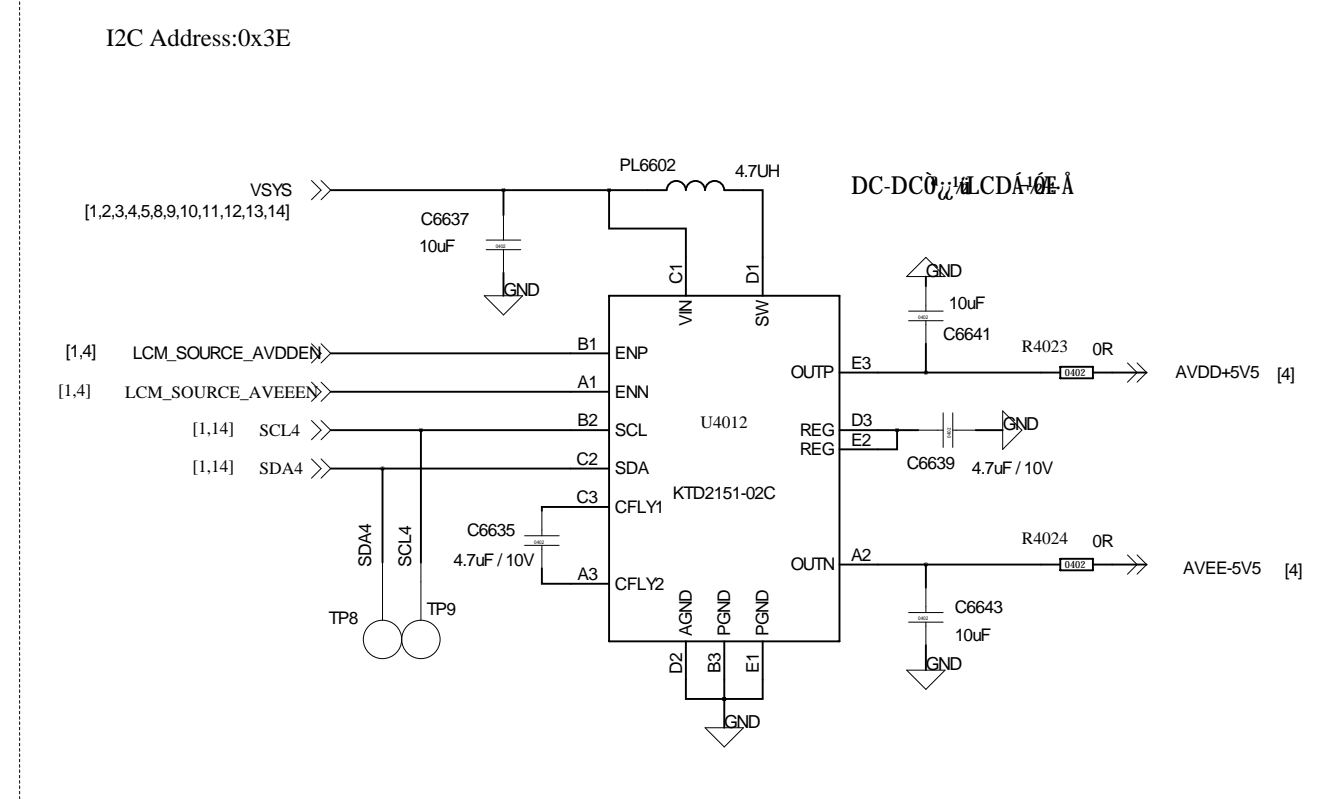
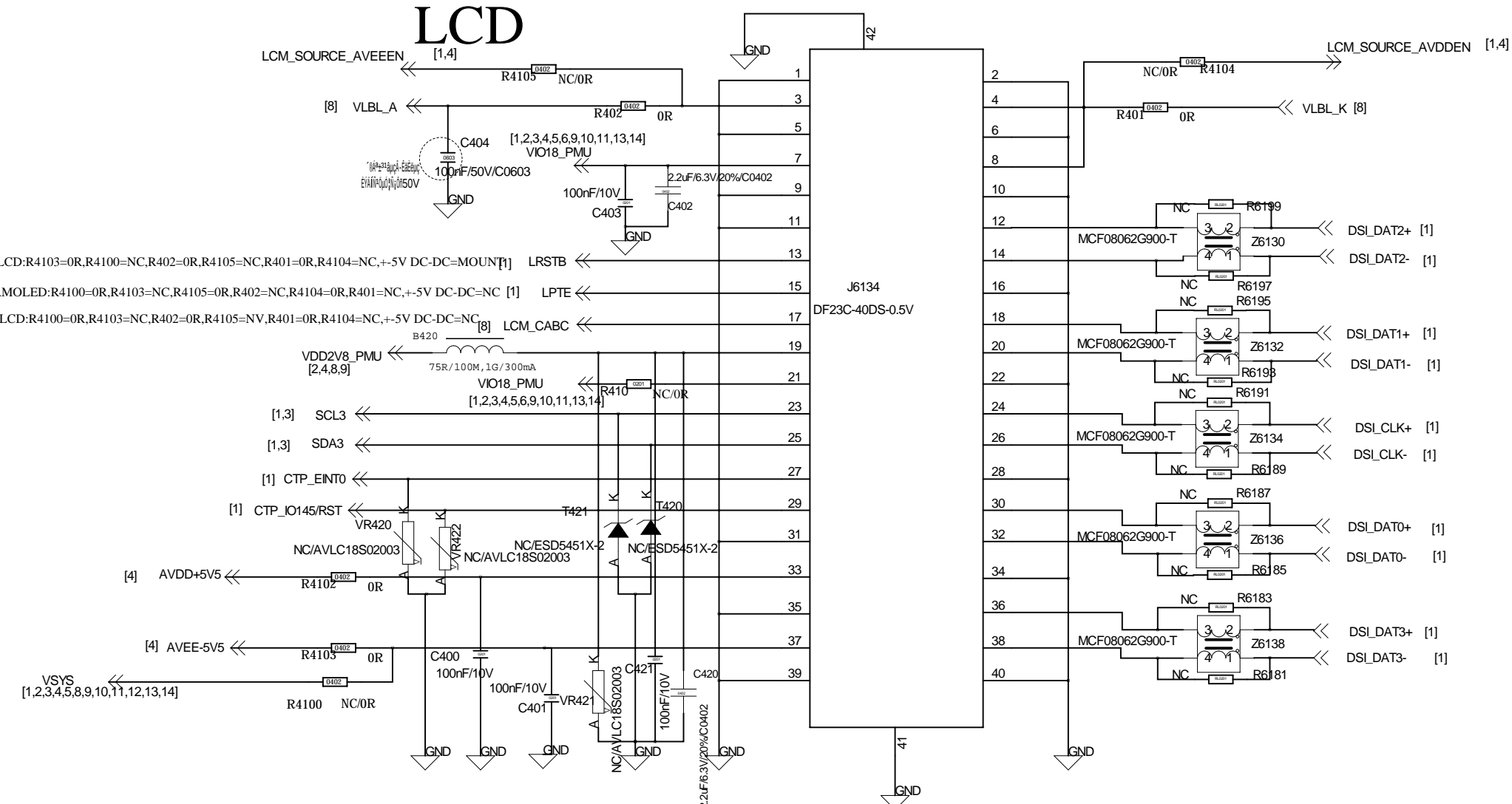


## Attention

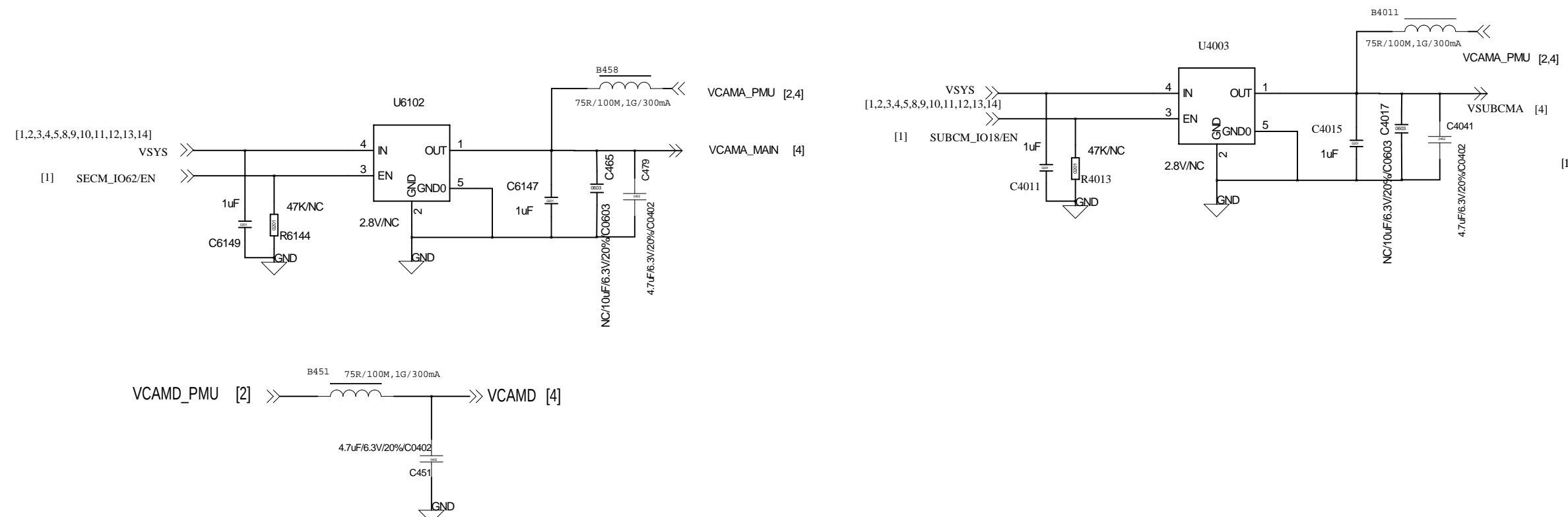
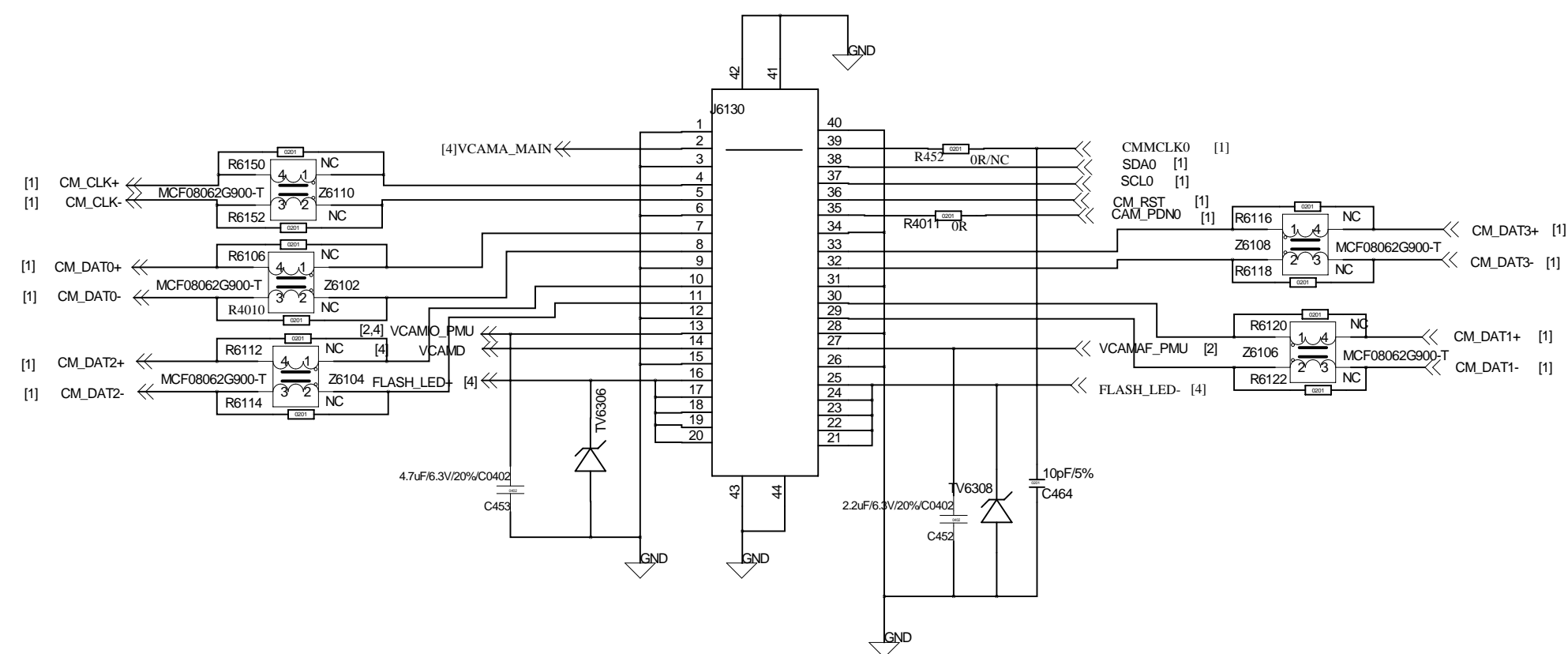




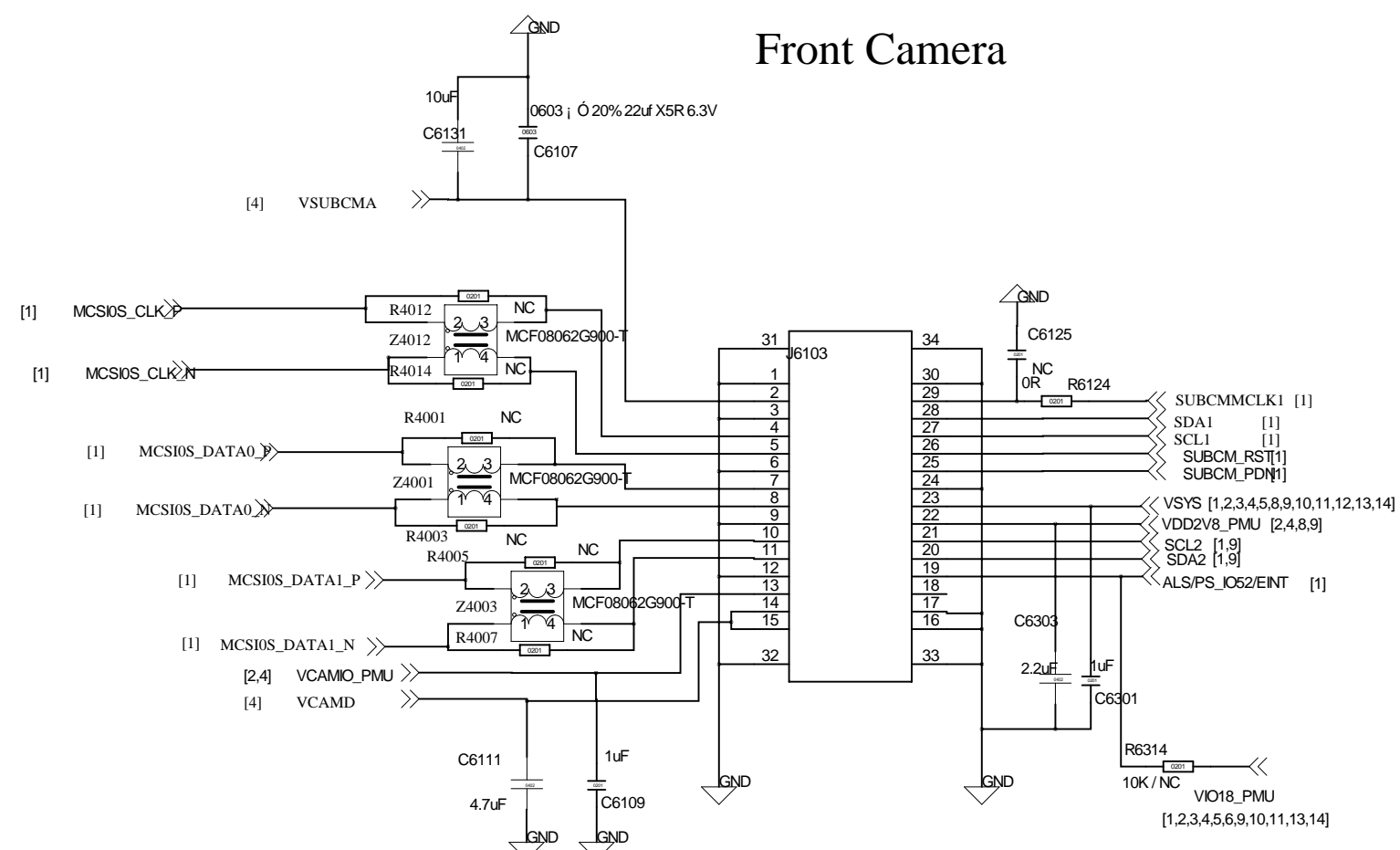
<div>设计</div> <div>校核</div> <div>审核</div> <div>会签</div> <div>标准化</div> <div>批准</div>		<div>DT02</div> <div>UGA-114</div> <div>AUDIO</div>	P/N:	
			版本:	V 1.0
			第 3 张	共 15 张
			深圳多务通信科技有限公司	



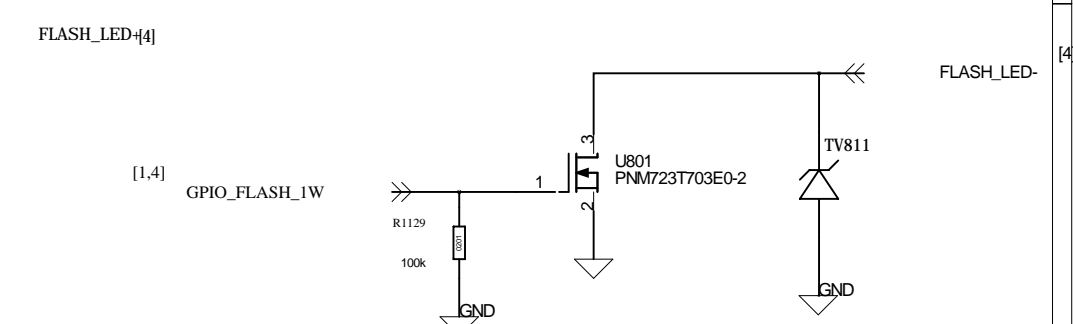
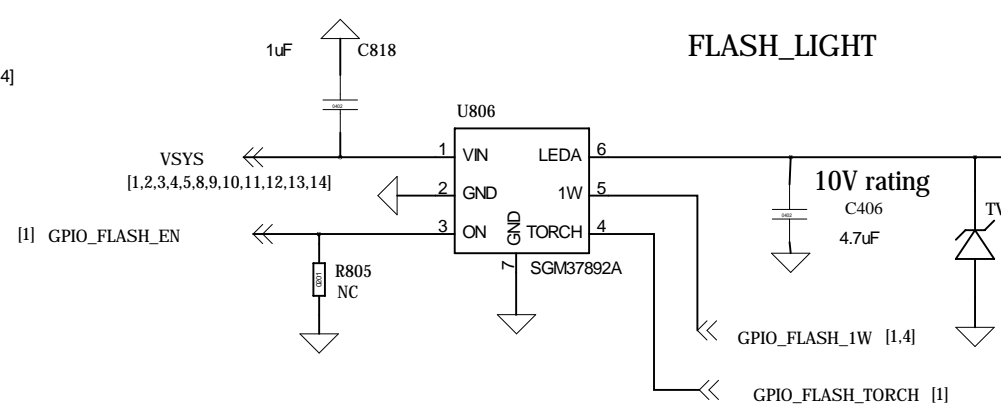
## Rear Camera-MASTER




## Front Camera



## FLASH\_LIGHT



<div>设计</div> <div>校核</div> <div>审核</div> <div>会签</div> <div>标准化</div> <div>批准</div>		<div>DT02</div> <div>  </div> <div>LCD, CTP, CM</div> <div>key_Sensor</div>	<div>P/N:</div> <div>版本: V 1.0</div> <div>第 4 张 共 15 张</div> <div>深圳大为通信科技有限公司</div>	

Please refer to QVL table to select LPDDR4(X) was verified by us.

U6000-A

MEM-LPDDR4X+EMMC5-KMCD6001M-B30

**LPDDR4+EMMC5**

CH-A LANE0		CH-B LANE0	
[1] EMDQ0_A << A3	DQ0_A	DQ0_B	AD3 >>> EMDQ0_B
[1] EMDQ1_A << B3	DQ1_A	DQ1_B	AC3 >>> EMDQ1_B
[1] EMDQ2_A << C3	DQ2_A	DQ2_B	AB3 >>> EMDQ2_B
[1] EMDQ3_A << D3	DQ3_A	DQ3_B	AA3 >>> EMDQ3_B
[1] EMDQ4_A << B7	DQ4_A	DQ4_B	AC7 >>> EMDQ4_B
[1] EMDQ5_A << C6	DQ5_A	DQ5_B	AB6 >>> EMDQ5_B
[1] EMDQ6_A << D7	DQ6_A	DQ6_B	AA7 >>> EMDQ6_B
[1] EMDQ7_A << C8	DQ7_A	DQ7_B	AB8 >>> EMDQ7_B
[1] EMDM0_A << D5	DM0_A	DM0_B	AA5 >>> EMDM0_B
[1] EMDQ50_A_P << C9	DQ50_T_A	DQ50_T_B	AB9 >>> EMDQ50_B_P
[1] EMDQ50_A_N << D9	DQ50_C_A	DQ50_C_B	AA9 >>> EMDQ50_B_N

CH-A LANE1		CH-B LANE1	
[1] EMDQ8_A << K5	DQ8_A	DQ8_B	R5 >>> EMDQ8_B
[1] EMDQ9_A << K6	DQ9_A	DQ9_B	R6 >>> EMDQ9_B
[1] EMDQ10_A << K3	DQ10_A	DQ10_B	R3 >>> EMDQ10_B
[1] EMDQ11_A << J3	DQ11_A	DQ11_B	T3 >>> EMDQ11_B
[1] EMDQ12_A << J7	DQ12_A	DQ12_B	T7 >>> EMDQ12_B
[1] EMDQ13_A << G3	DQ13_A	DQ13_B	Y3 >>> EMDQ13_B
[1] EMDQ14_A << H6	DQ14_A	DQ14_B	U6 >>> EMDQ14_B
[1] EMDQ15_A << H6	DQ15_A	DQ15_B	U8 >>> EMDQ15_B
[1] EMDM1_A << H3	DM1_A	DM1_B	U3 >>> EMDM1_B
[1] EMDQ51_A_P << K9	DQ51_T_A	DQ51_T_B	R9 >>> EMDQ51_B_P
[1] EMDQ51_A_N << J9	DQ51_C_A	DQ51_C_B	T9 >>> EMDQ51_B_N

CH-A CA		CH-B CA	
[1] EMCLK_A_P << H16	CK_T_A	CK_T_B	U16 >>> EMCLK_B_P
[1] EMCLK_A_N << G16	CK_C_A	CK_C_B	V16 >>> EMCLK_B_N
[1] EMCKE0_A << E16	CKE0_A	CKE0_B	Y16 >>> EMCKE0_B
[1] EMCKE1_A << F16	CKE1_A	CKE1_B	W16 >>> EMCKE1_B
[1] EMCS0_A << E15	CS0_A	CS0_B	T14 >>> EMCS0_B
[1] EMCS1_A << F15	CS1_A	CS1_B	W15 >>> EMCS1_B
[1] EMCA0_A << G14	CA0_A	CA0_B	V14 >>> EMCA0_B
[1] EMCA1_A << F13	CA1_A	CA1_B	W13 >>> EMCA1_B
[1] EMCA2_A << C13	CA2_A	CA2_B	AB13 >>> EMCA2_B
[1] EMCA3_A << D13	CA3_A	CA3_B	AA13 >>> EMCA3_B
[1] EMCA4_A << E13	CA4_A	CA4_B	Y13 >>> EMCA4_B
[1] EMCA5_A << C15	CA5_A	CA5_B	AB15 >>> EMCA5_B

**Control Signals**

Signal	Pin	Connection
RST	AA16	EMRSTN
RESET_N	AA16	EMRSTN

**Power Management**

VDDMEM << R0519 (100nF) << R0518 (100nF) << J13

VDDQ << R0512 (240) << B16 << Z0X\_A

VDDQ << R0502 (240) << C16 << Z0Y\_A

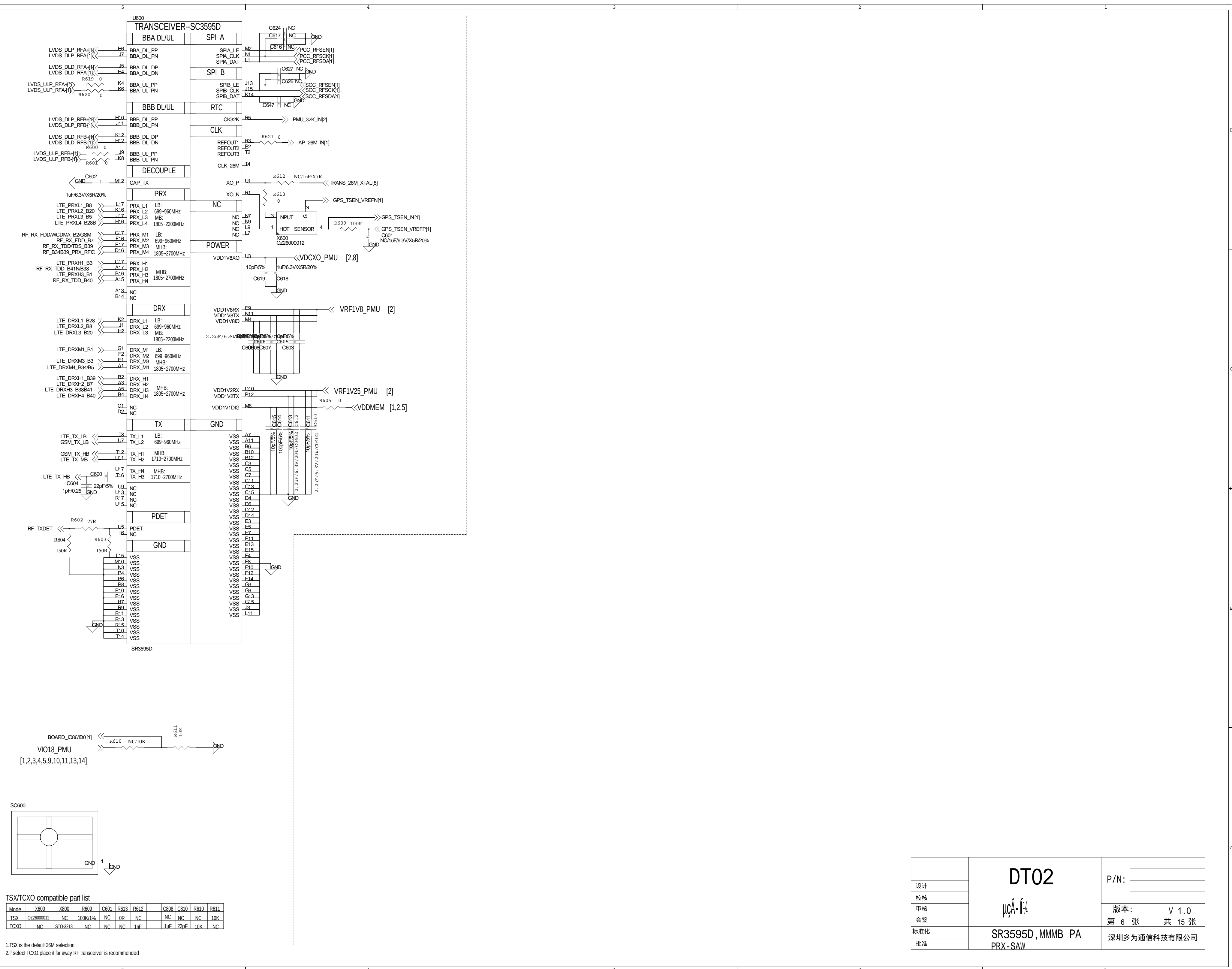
VDDQ << R0511 (240nF) << D16 << NC

V1018\_P/U << R0508 (1.5V) << R0507 (100nF) << T0500 << T0502

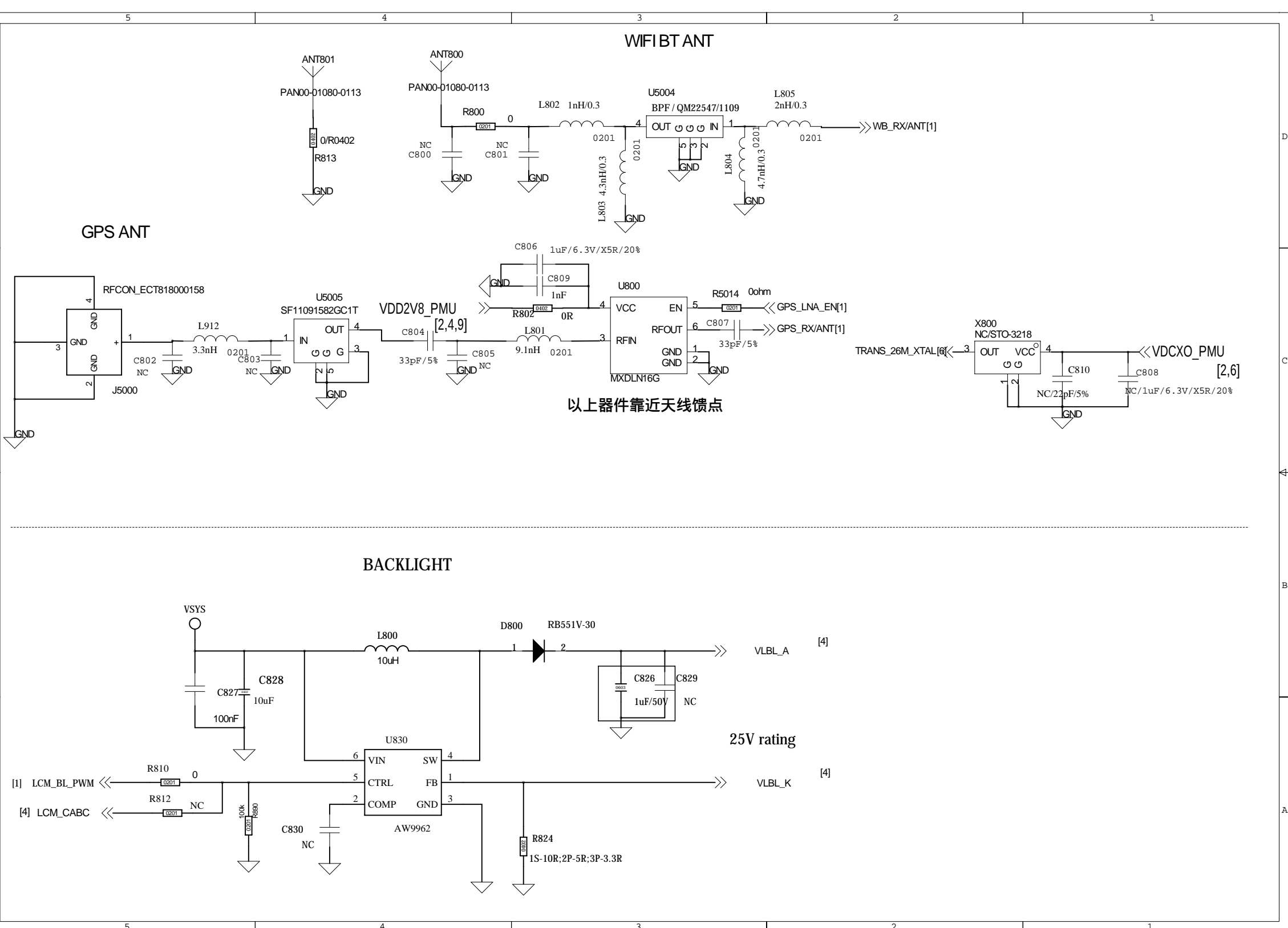


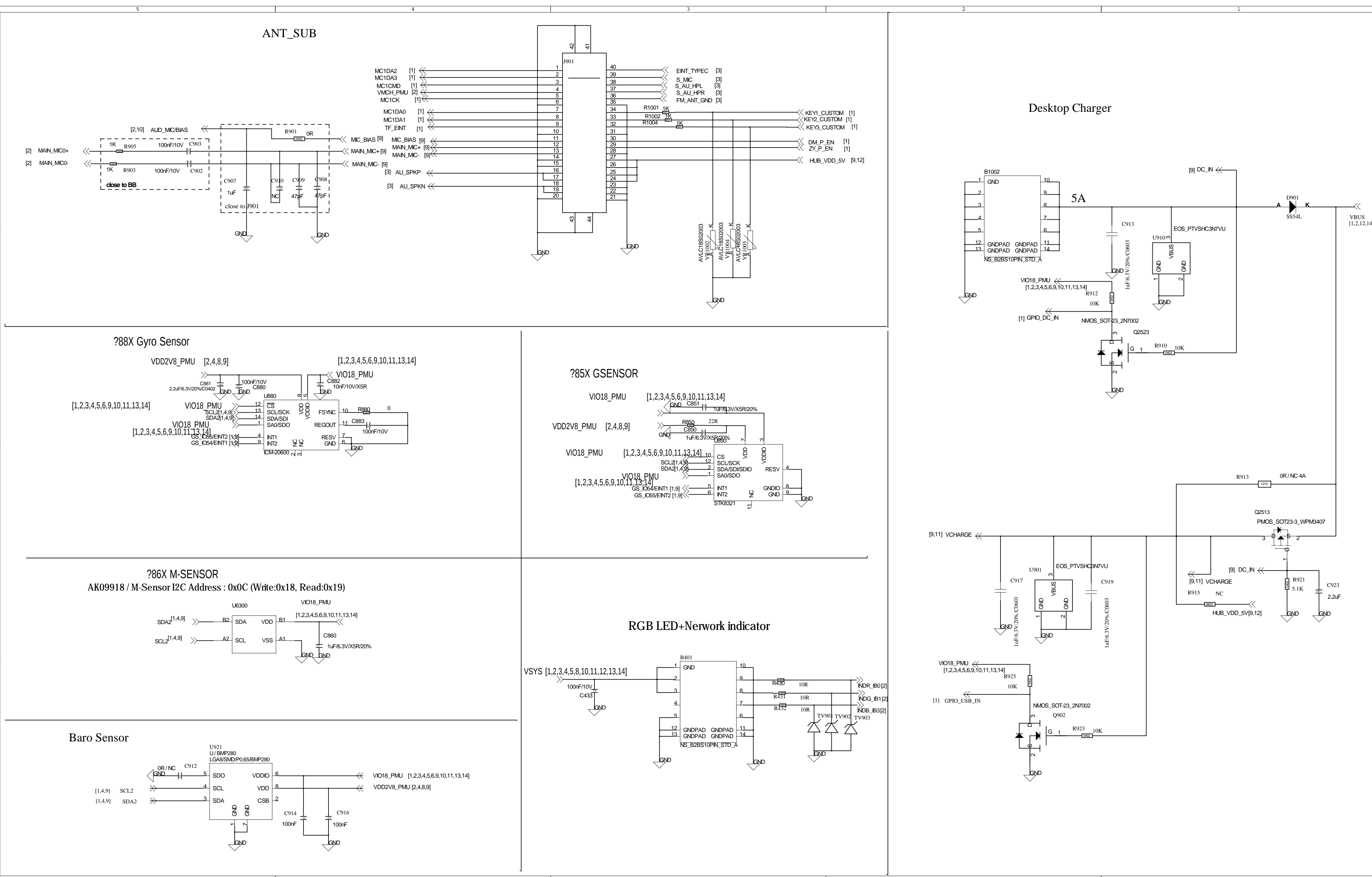
BUCK 0.9V output for BB VDDMEMQ(LPDDR4 reserve):

R0504 value is half of R0501 to set output voltage as 0.9V.

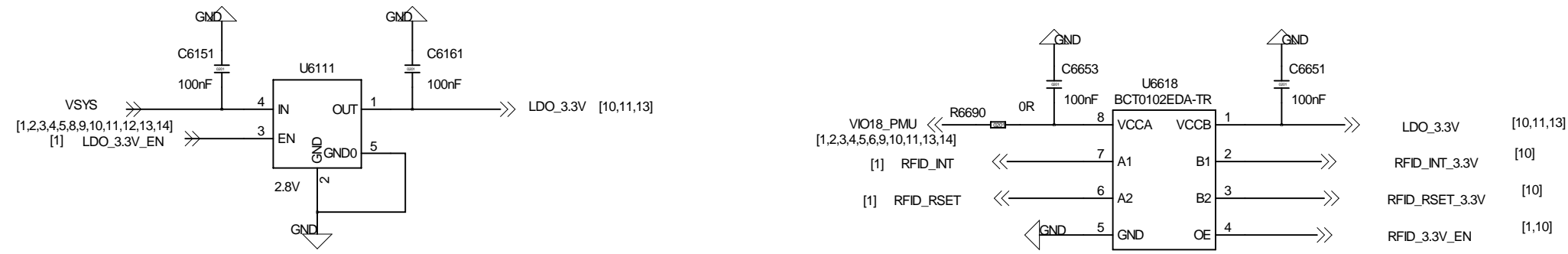






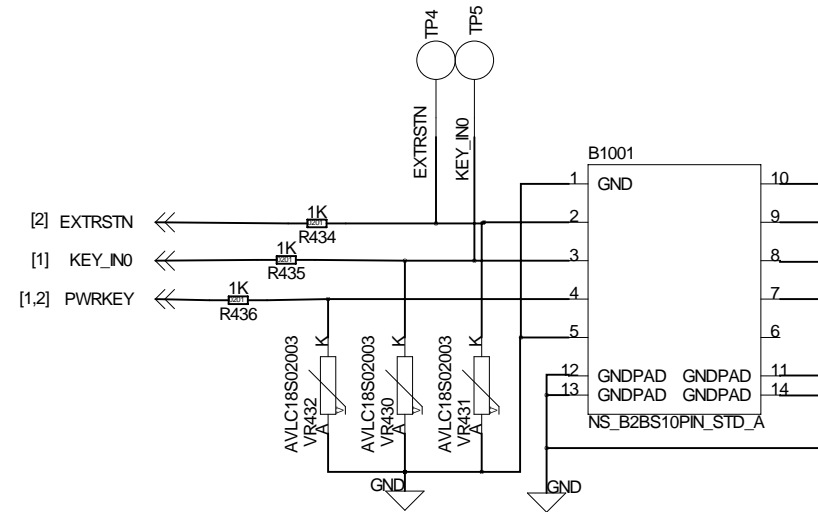


# RFID

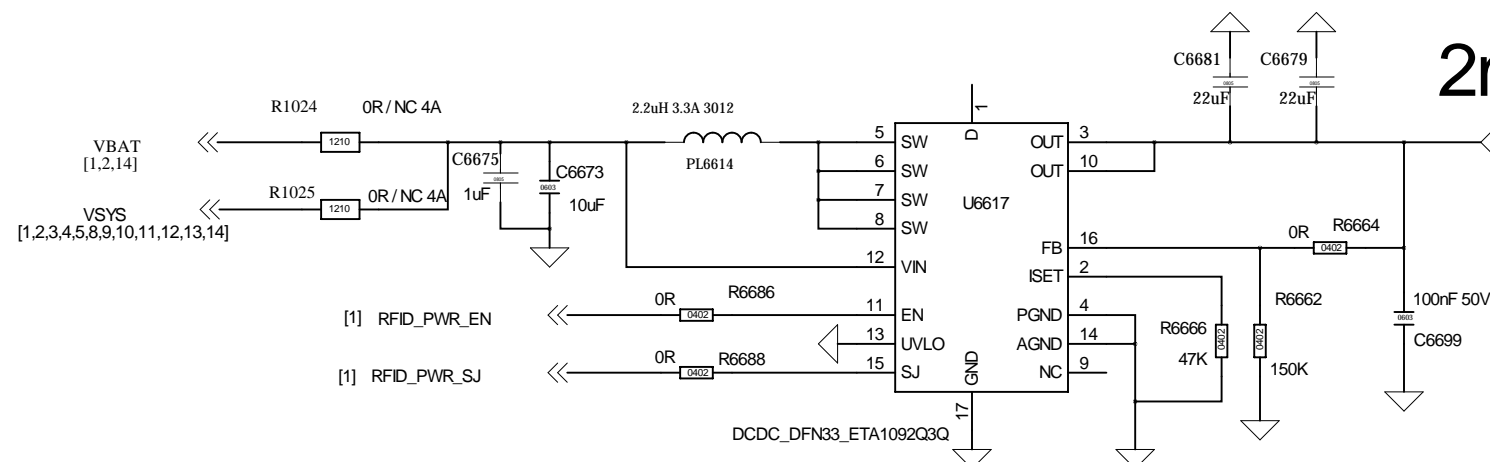


743X SIDE KEY

EXTRSTN+GND=UP  
KEY\_IN+GND=DOWN

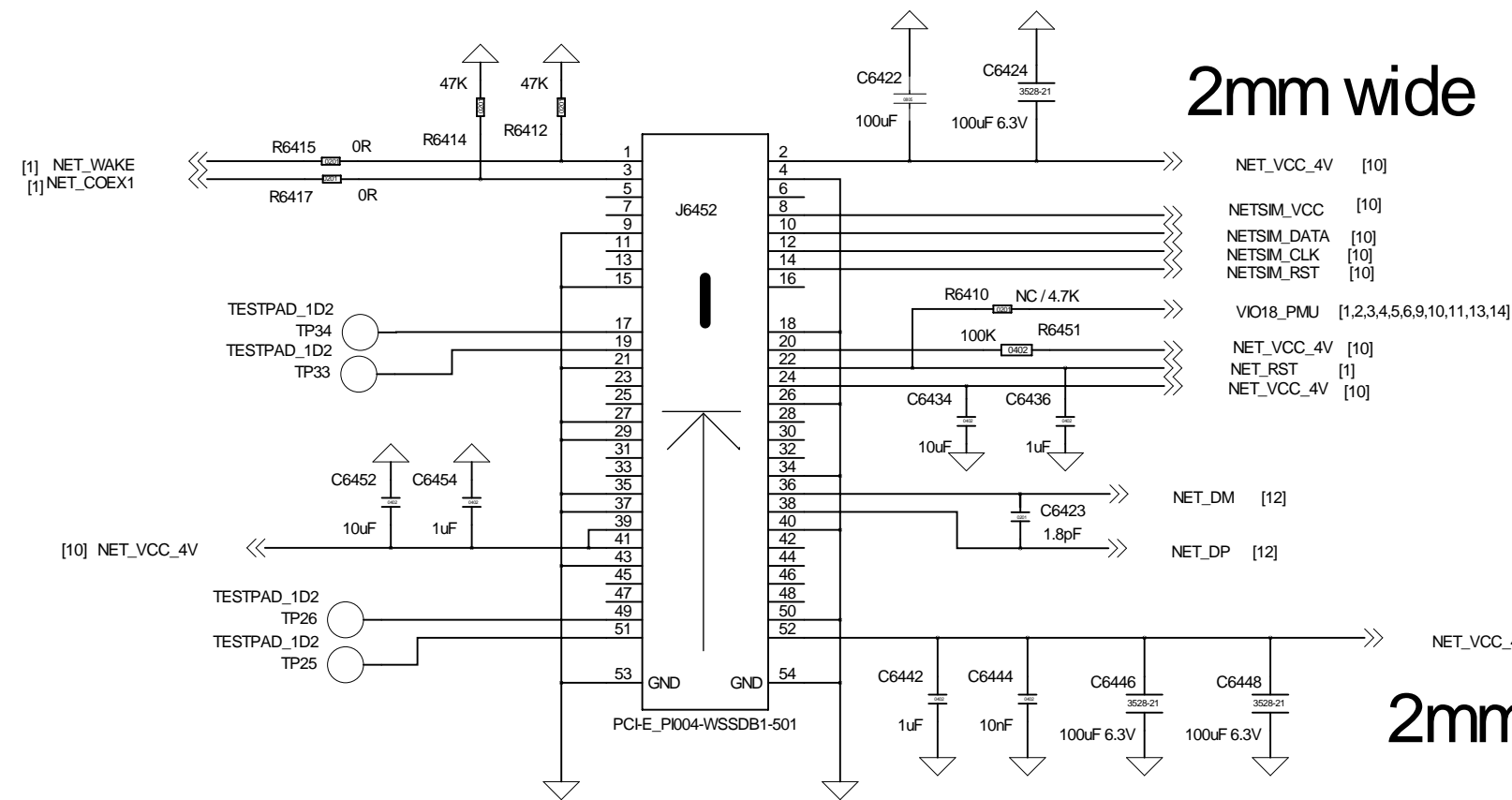


2mm wide



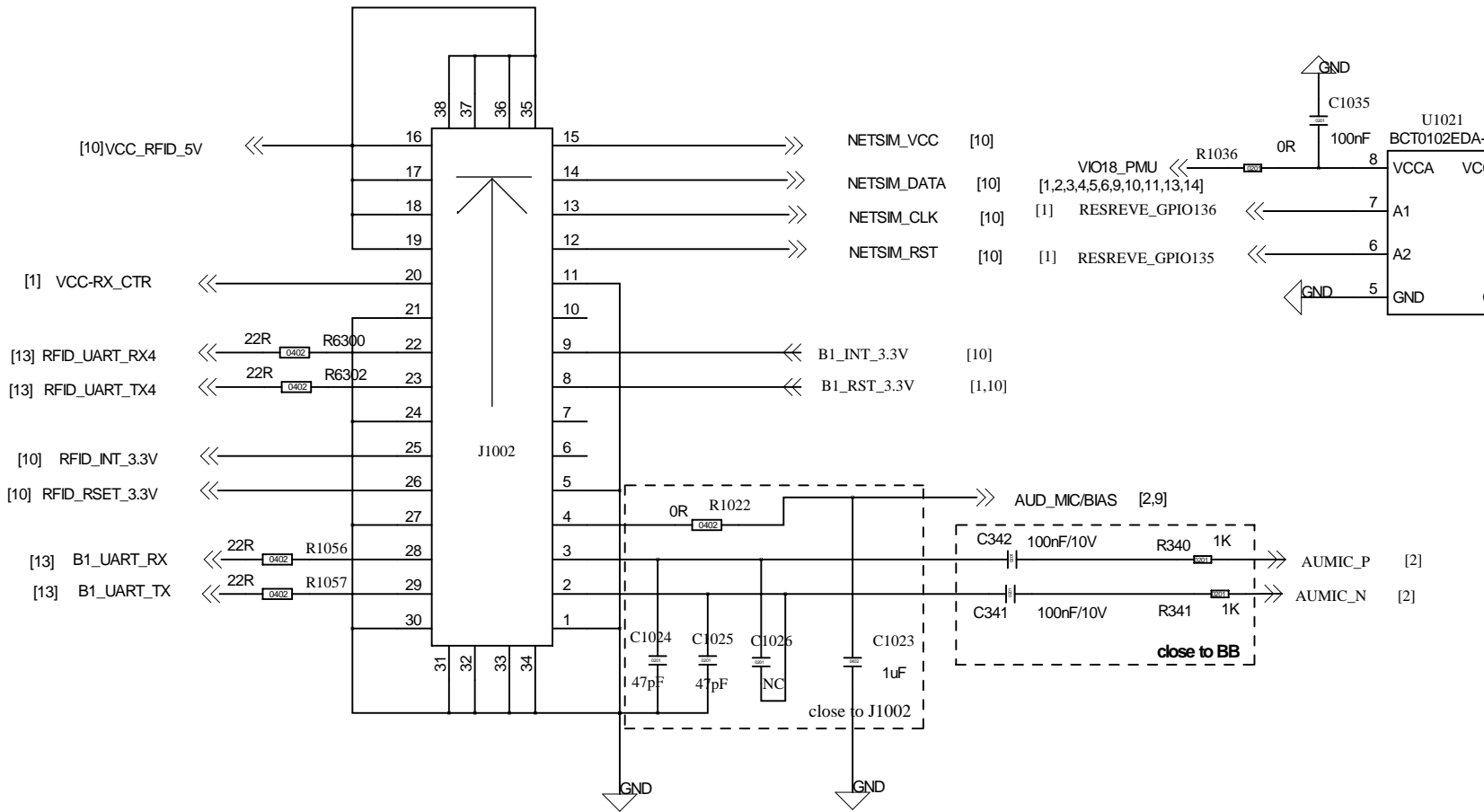
## PCIE CON

2mm wide

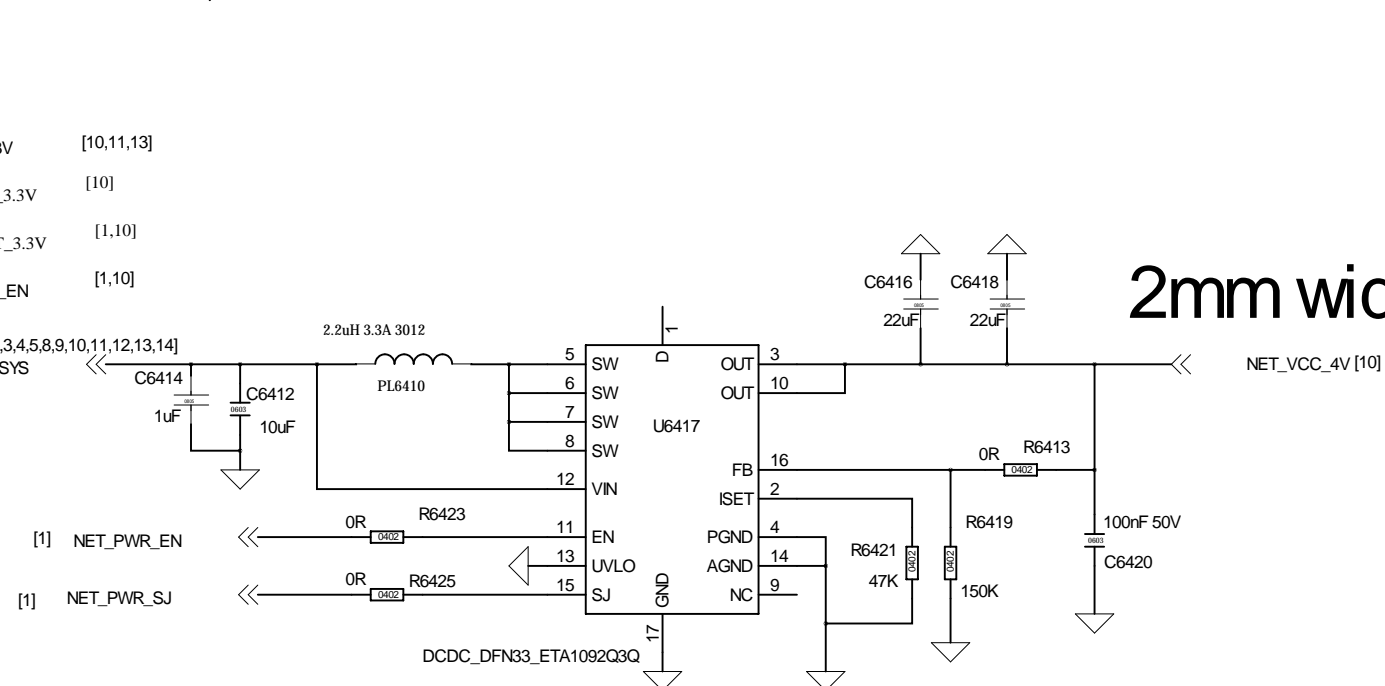


2mm wide

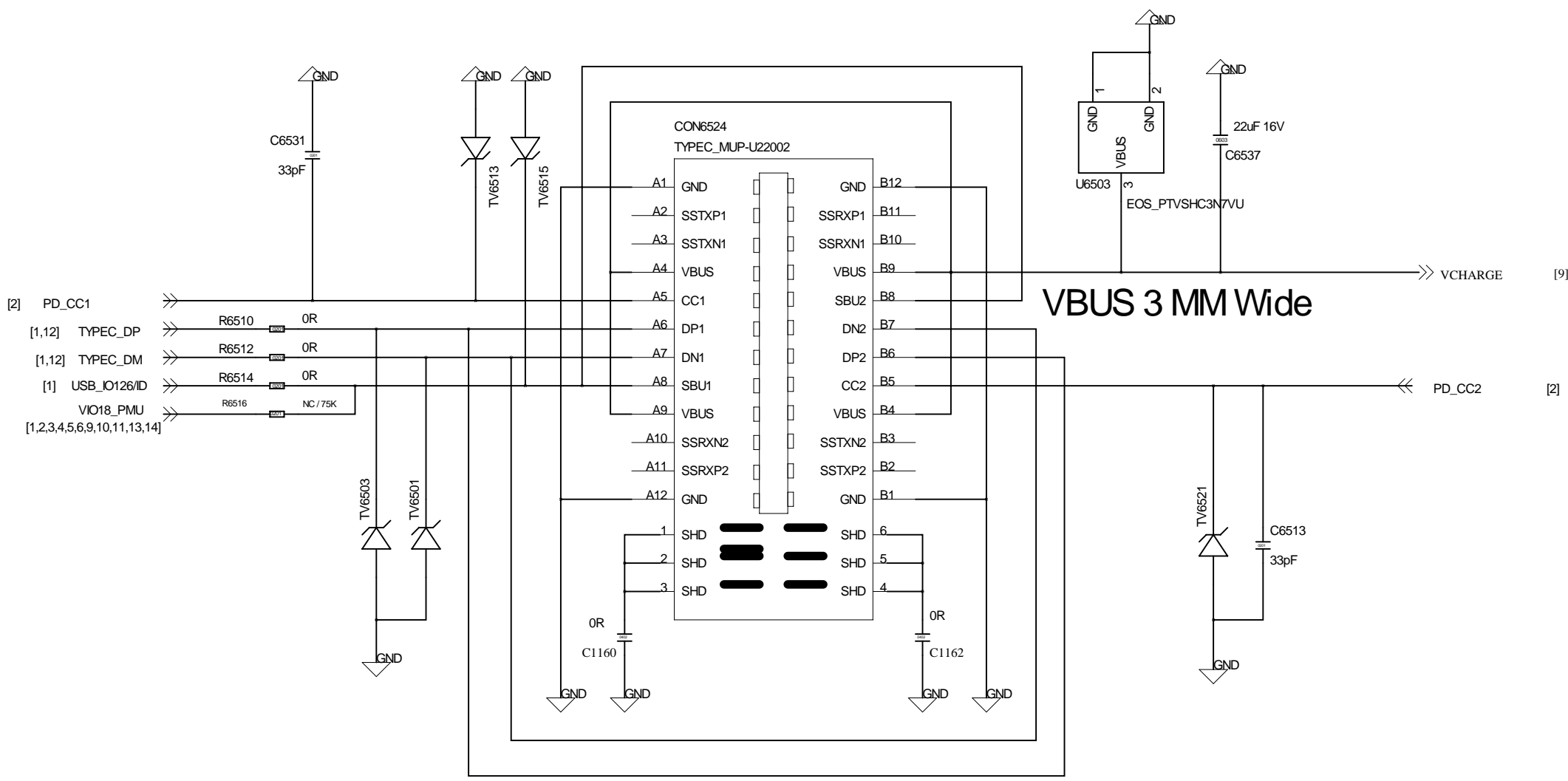
RFID SUB



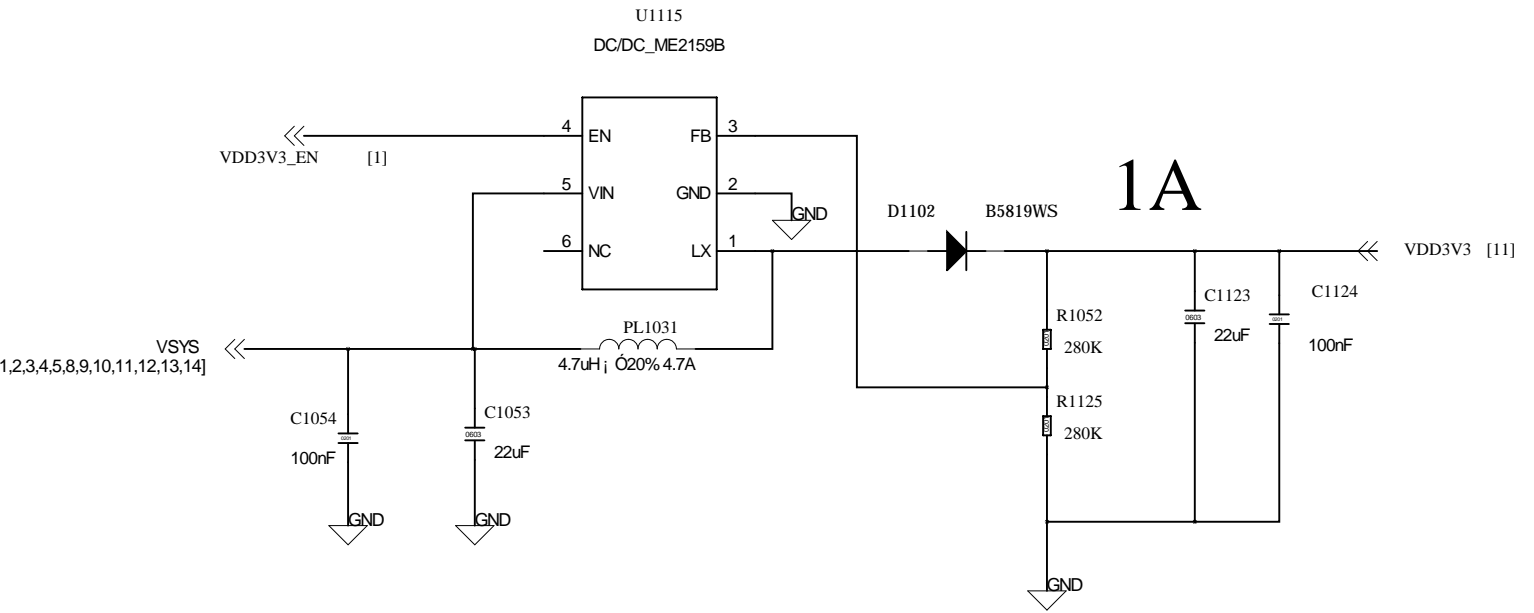
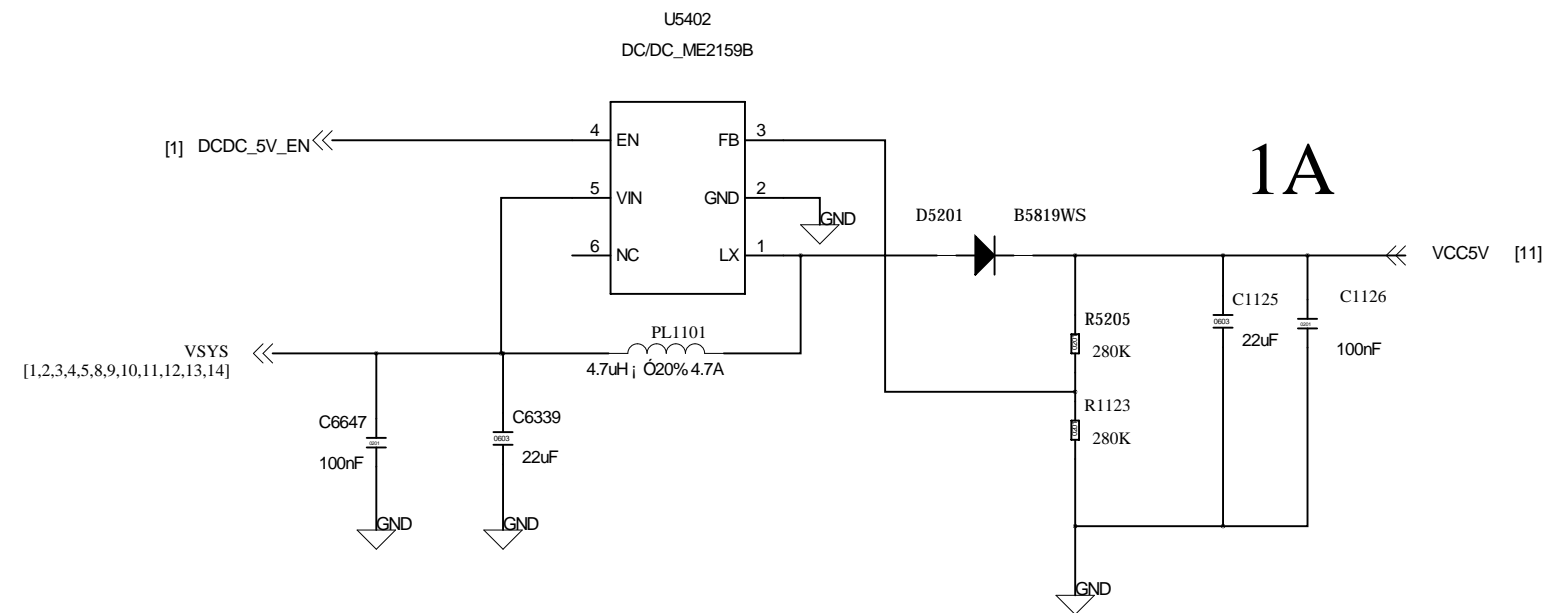
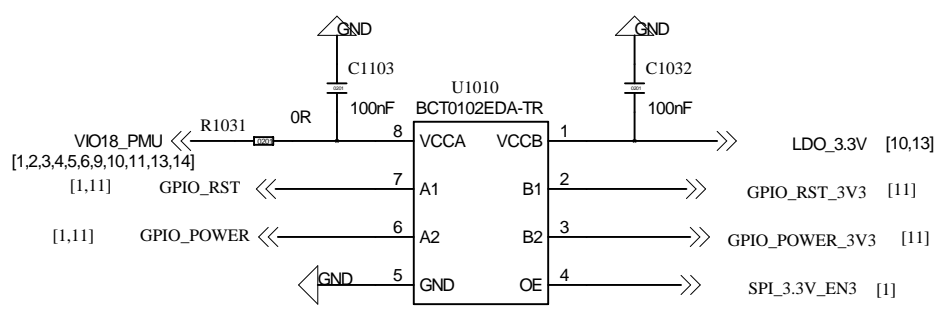
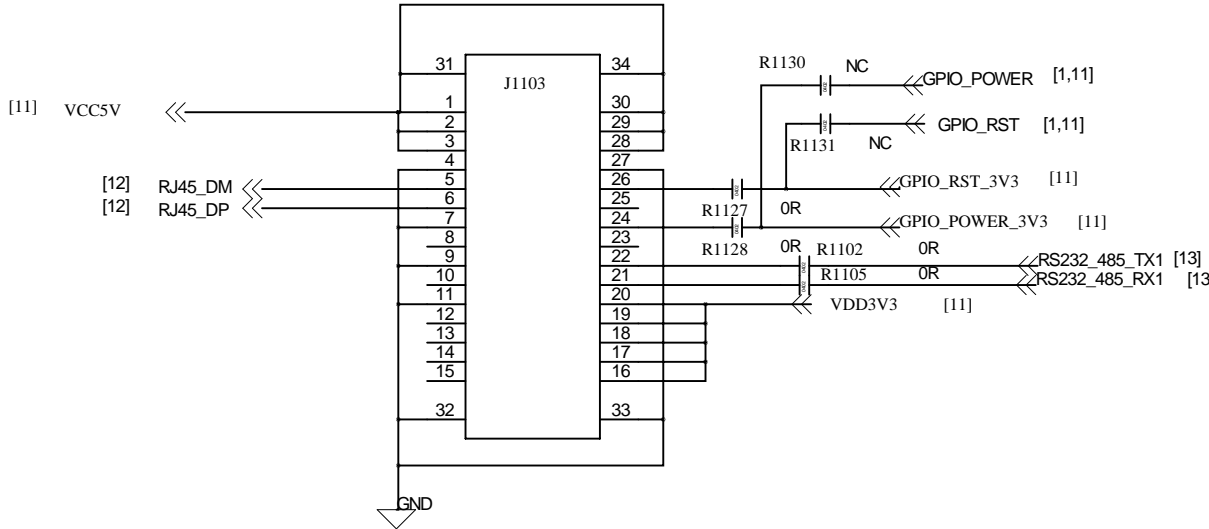
2mm wide



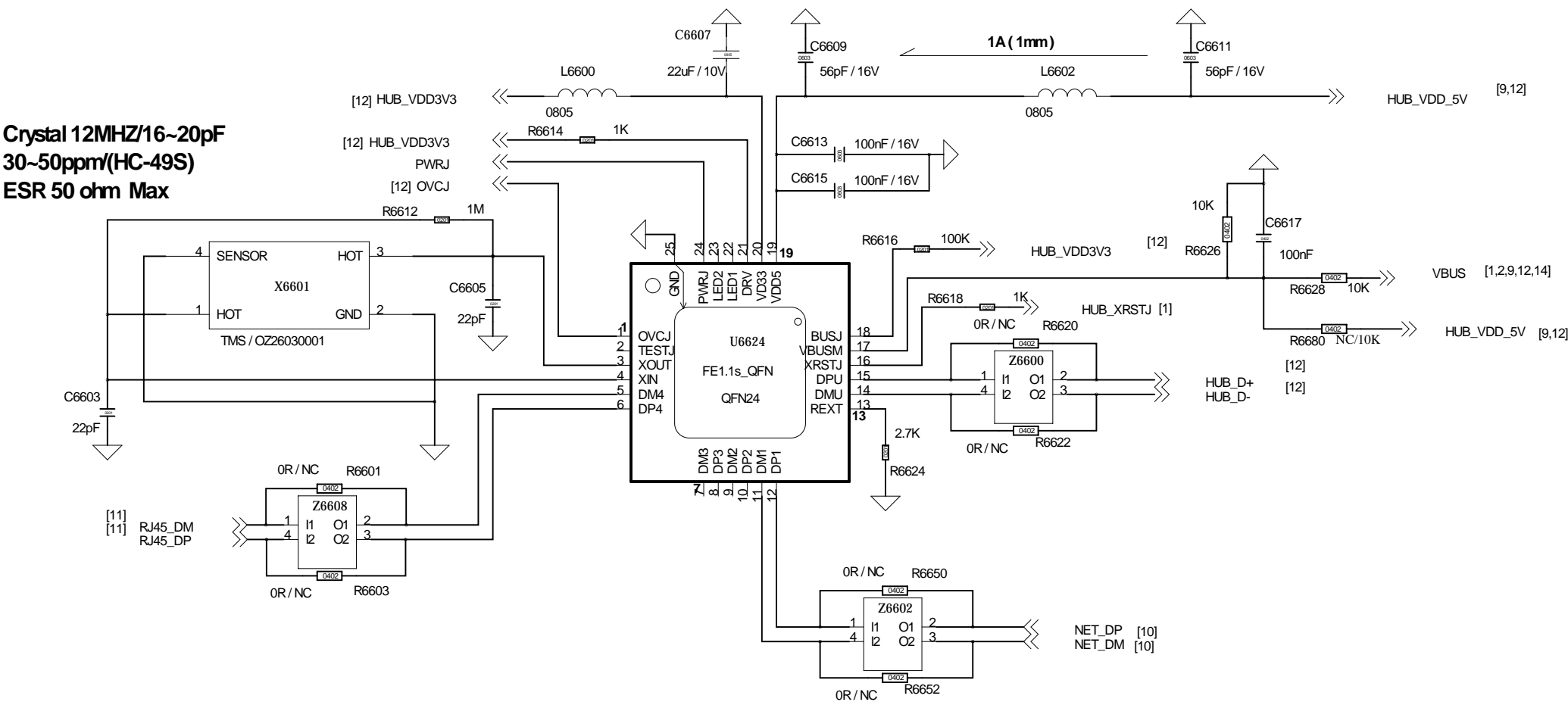
TYPE-C USB2.0



POGOPIN-CONNECT

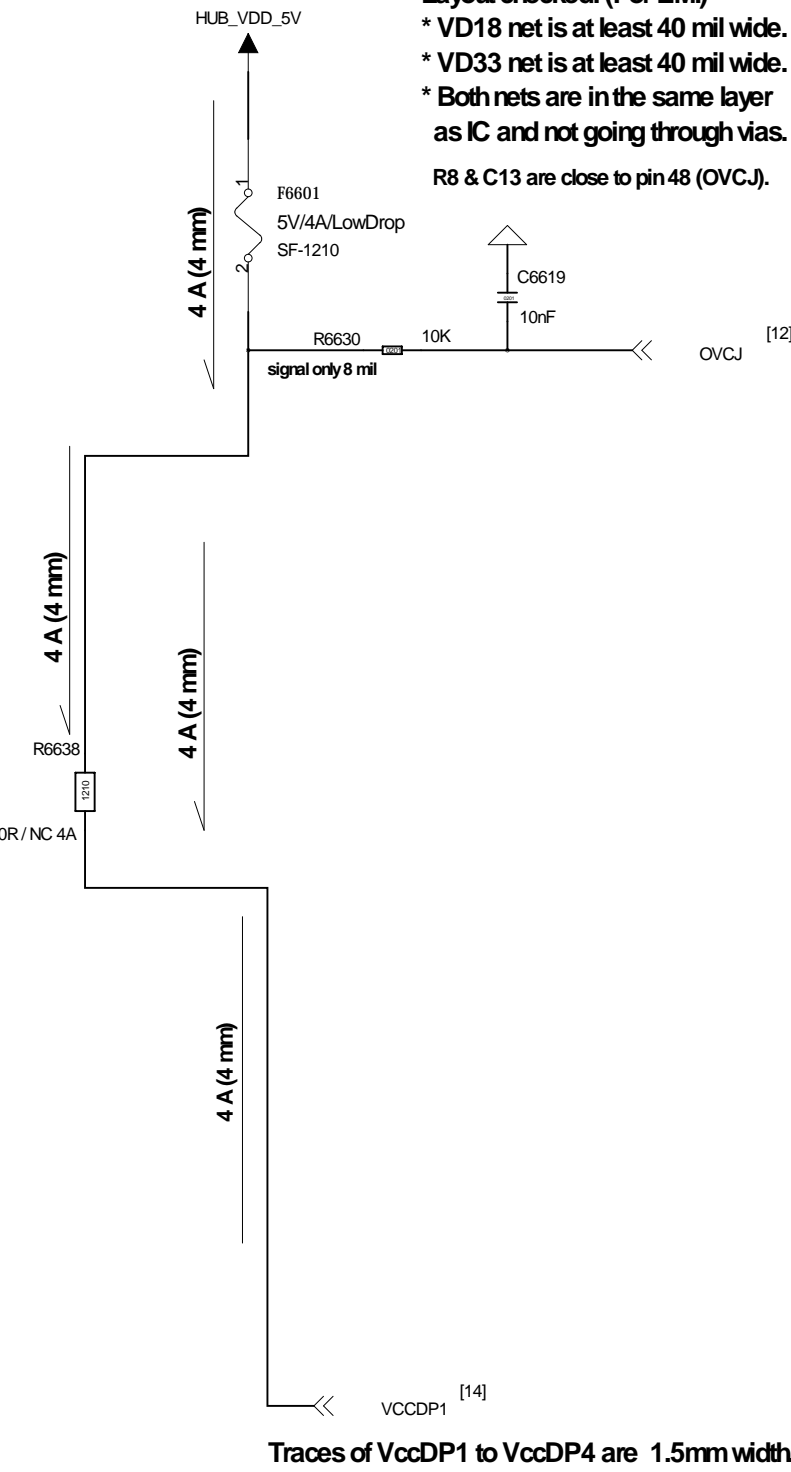
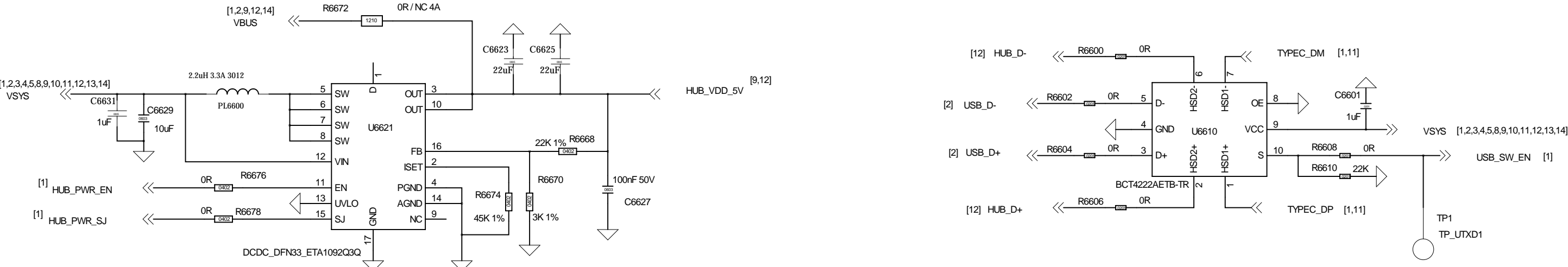


USB HUB



Layout checked: (For EMI)  
\* VD18 net is at least 40 mil wide.  
\* VD33 net is at least 40 mil wide.  
\* Both nets are in the same layer  
as IC and not going through vias.  
R8 & C13 are close to pin 48 (OVCJ).

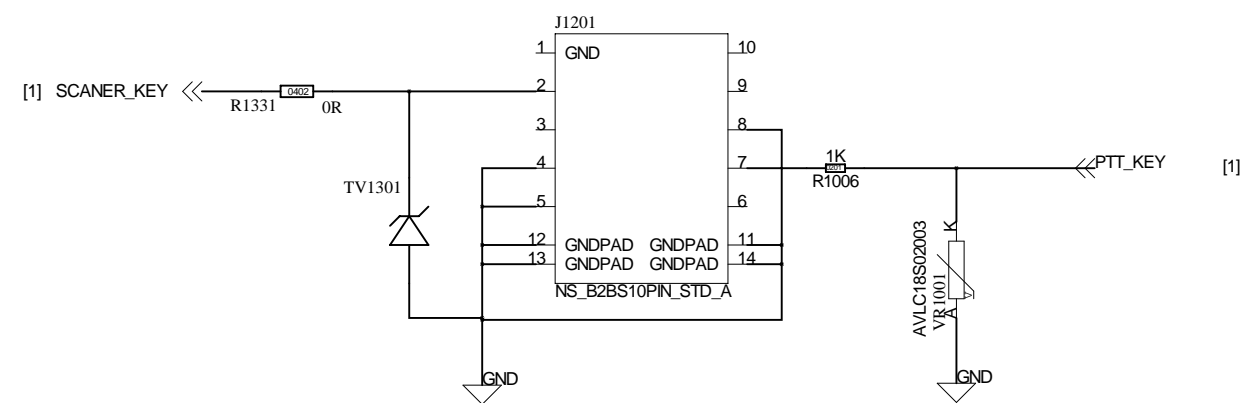
USB HUB POWER



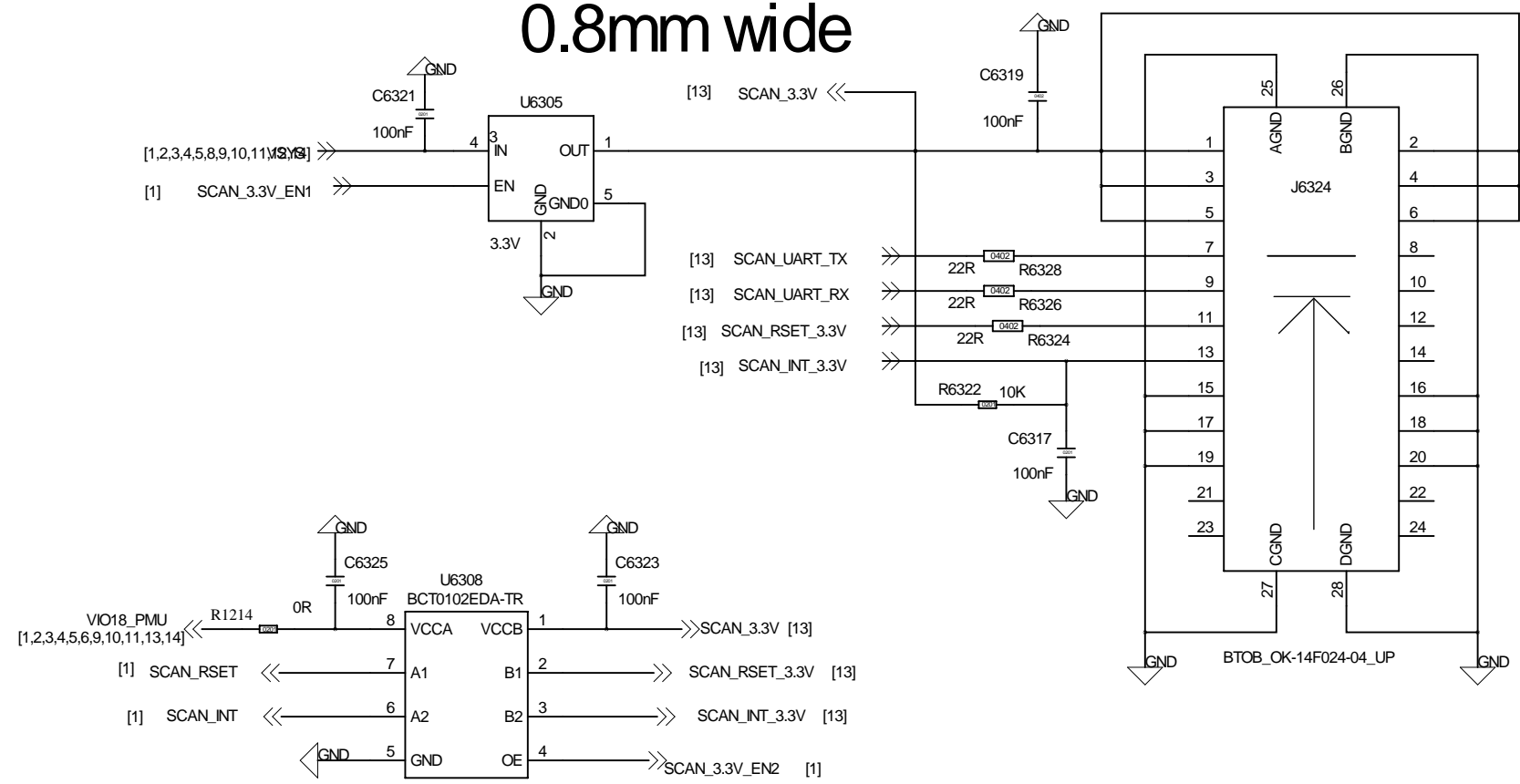
Traces of VccDP1 to VccDP4 are 1.5mm width

ID Scanner

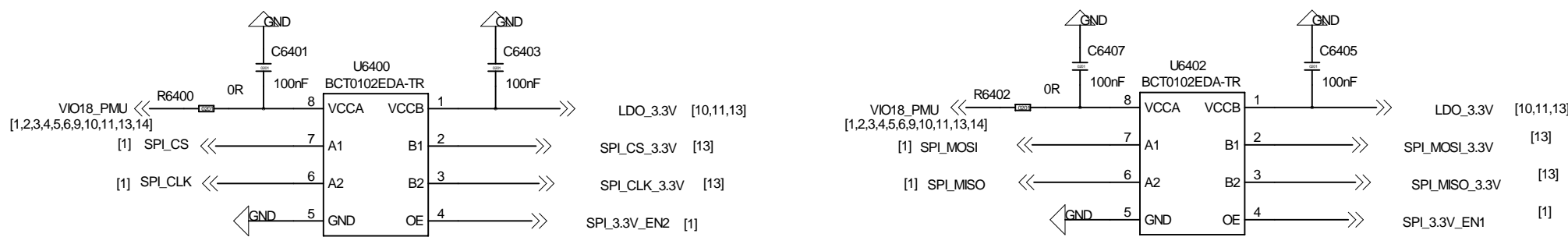
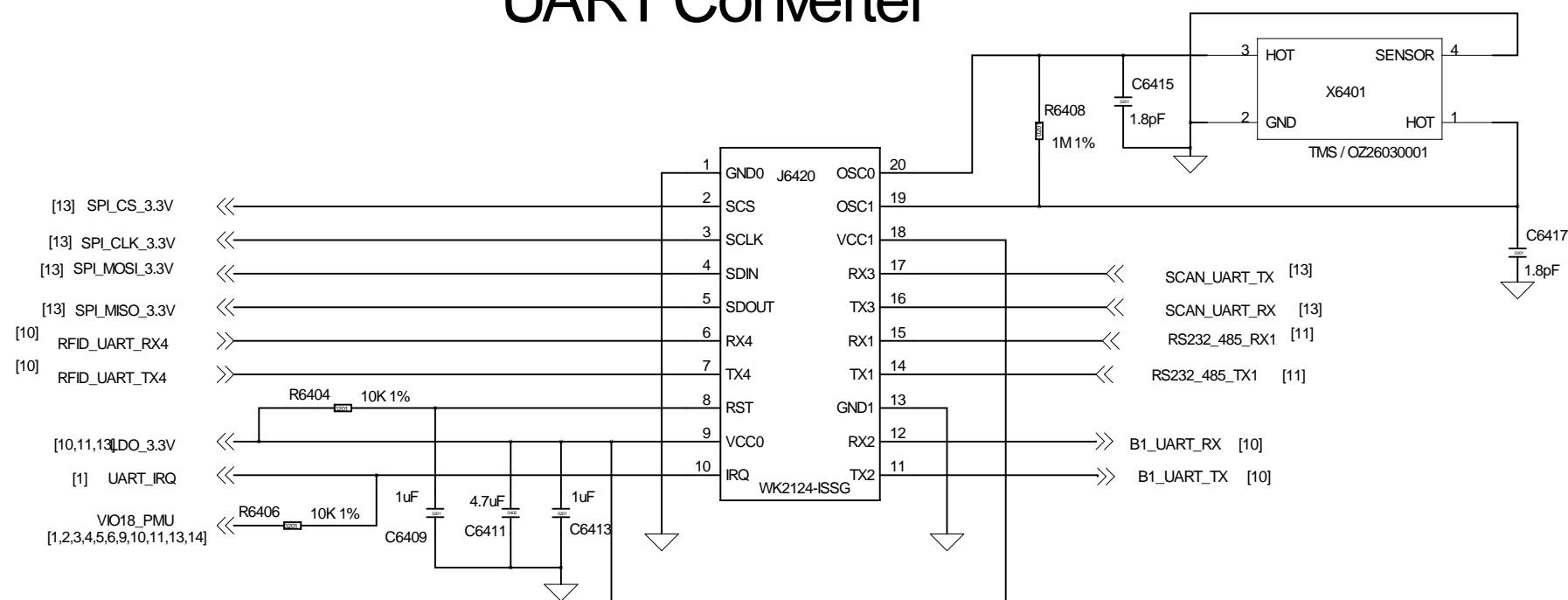
Scanner Key



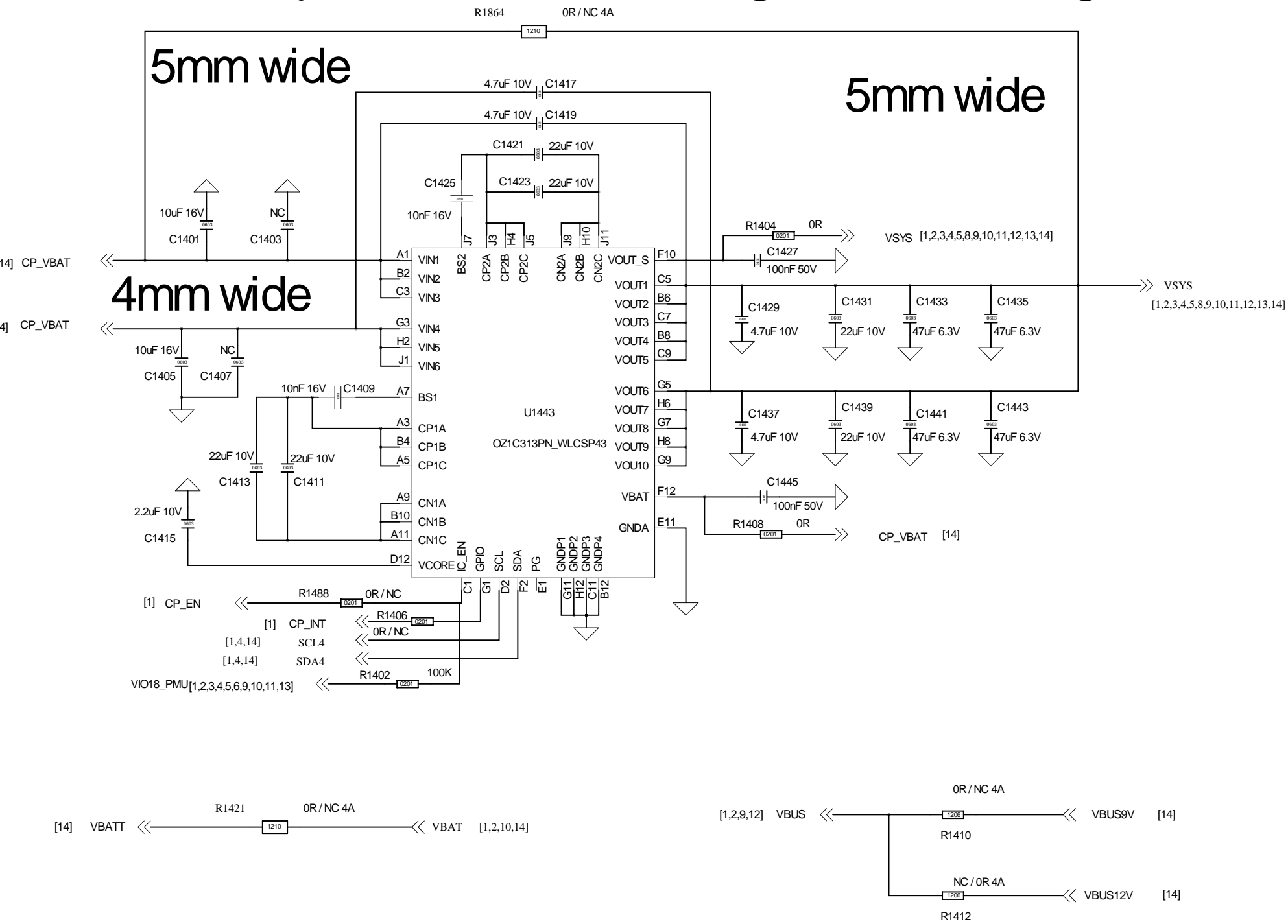
0.8mm wide



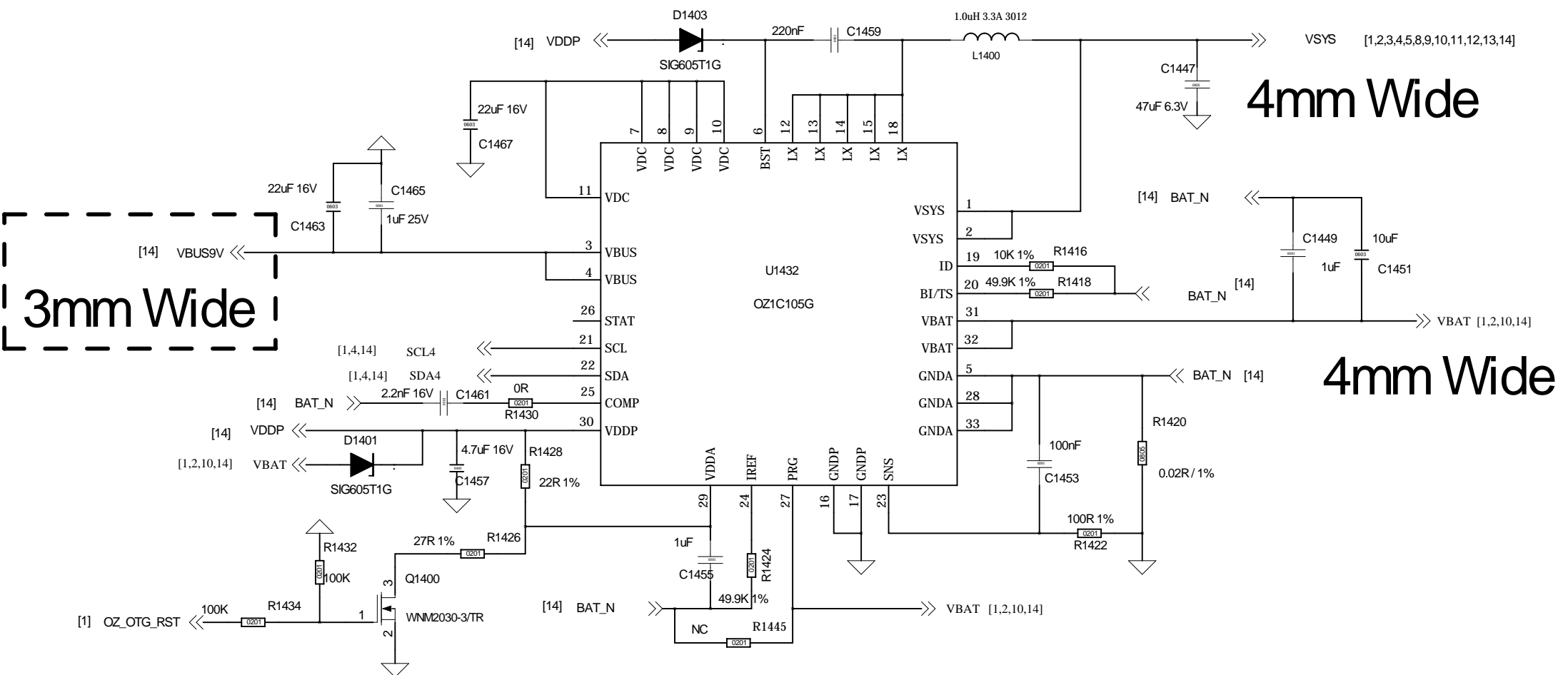
UART Converter



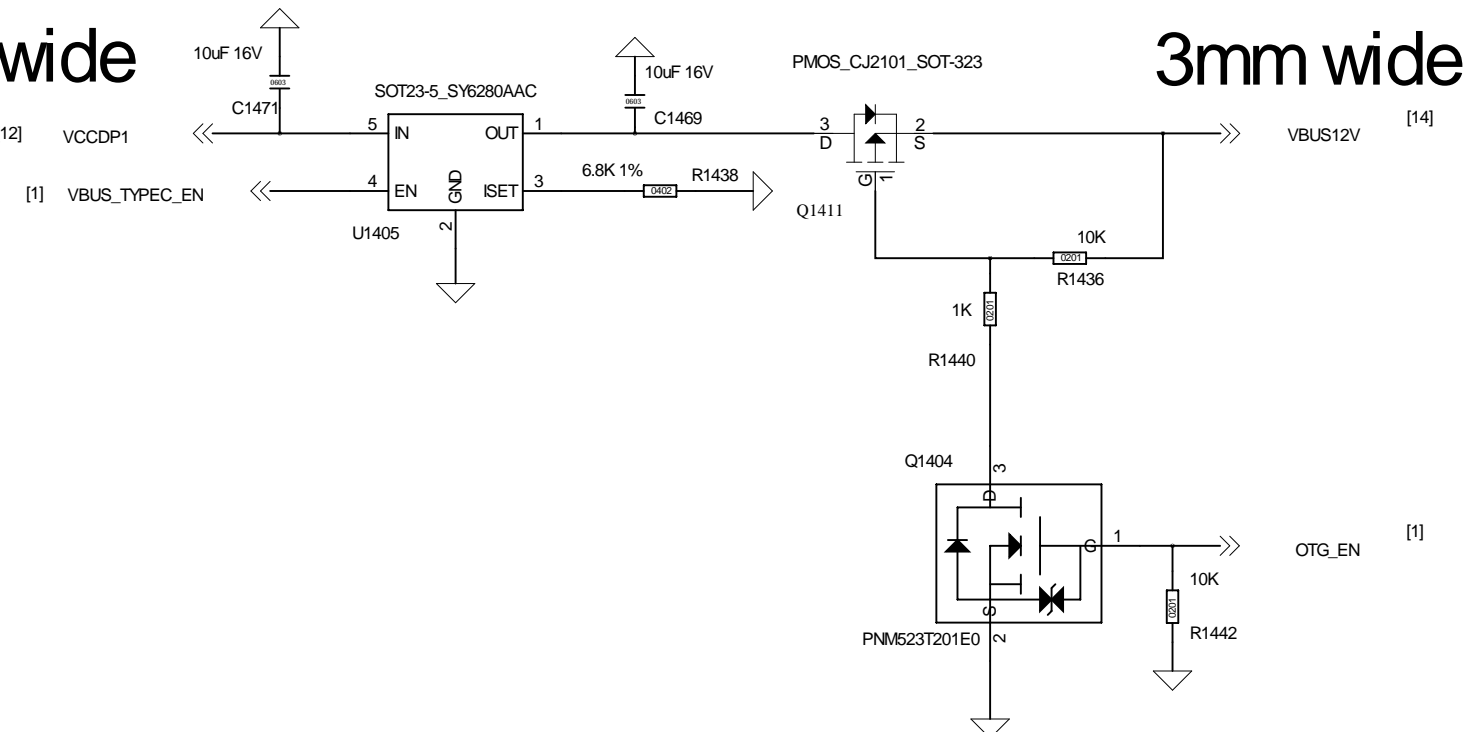
Main Battery Reduction Voltage And Charger



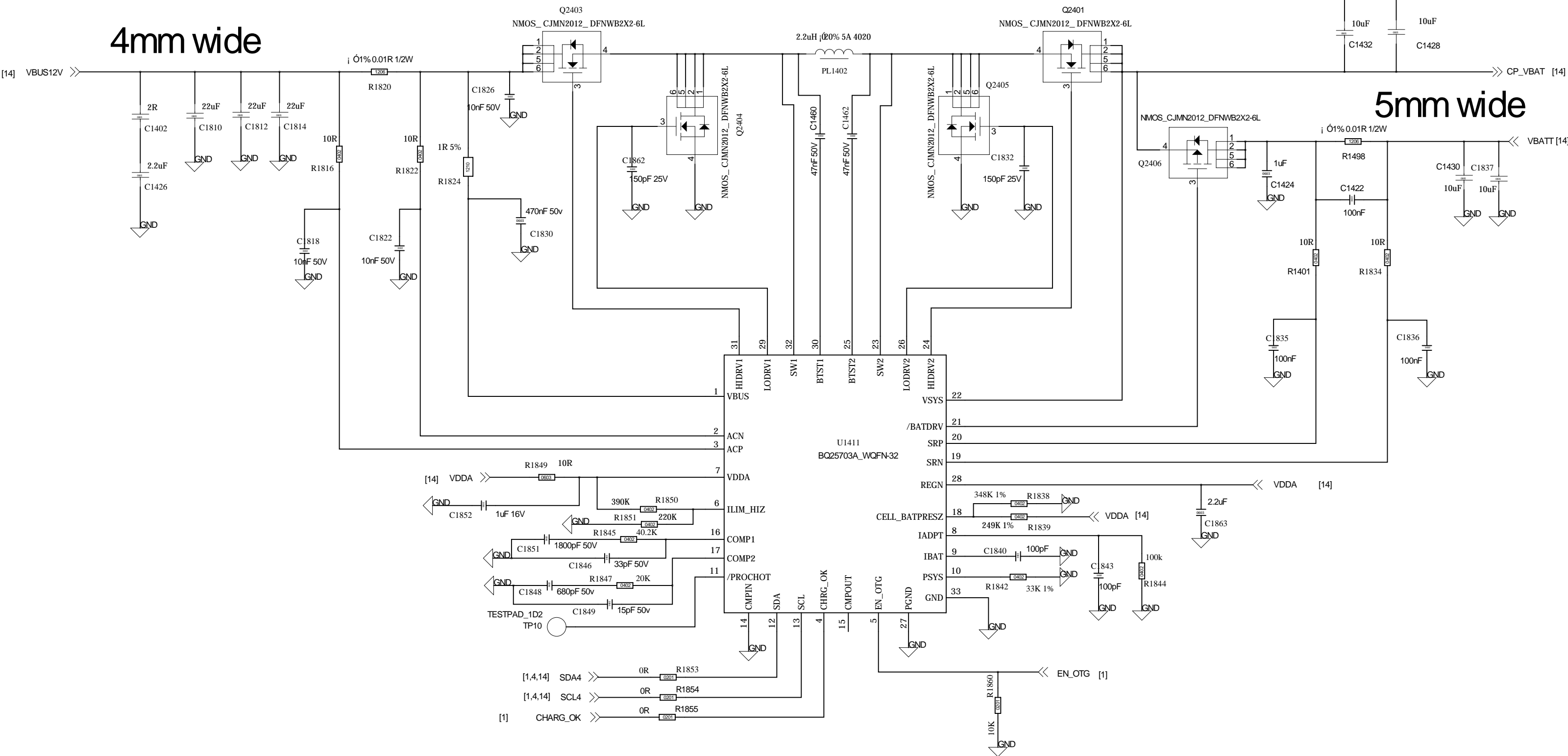
4.3V Single Battery 9V / 2A Charger



12V OTG

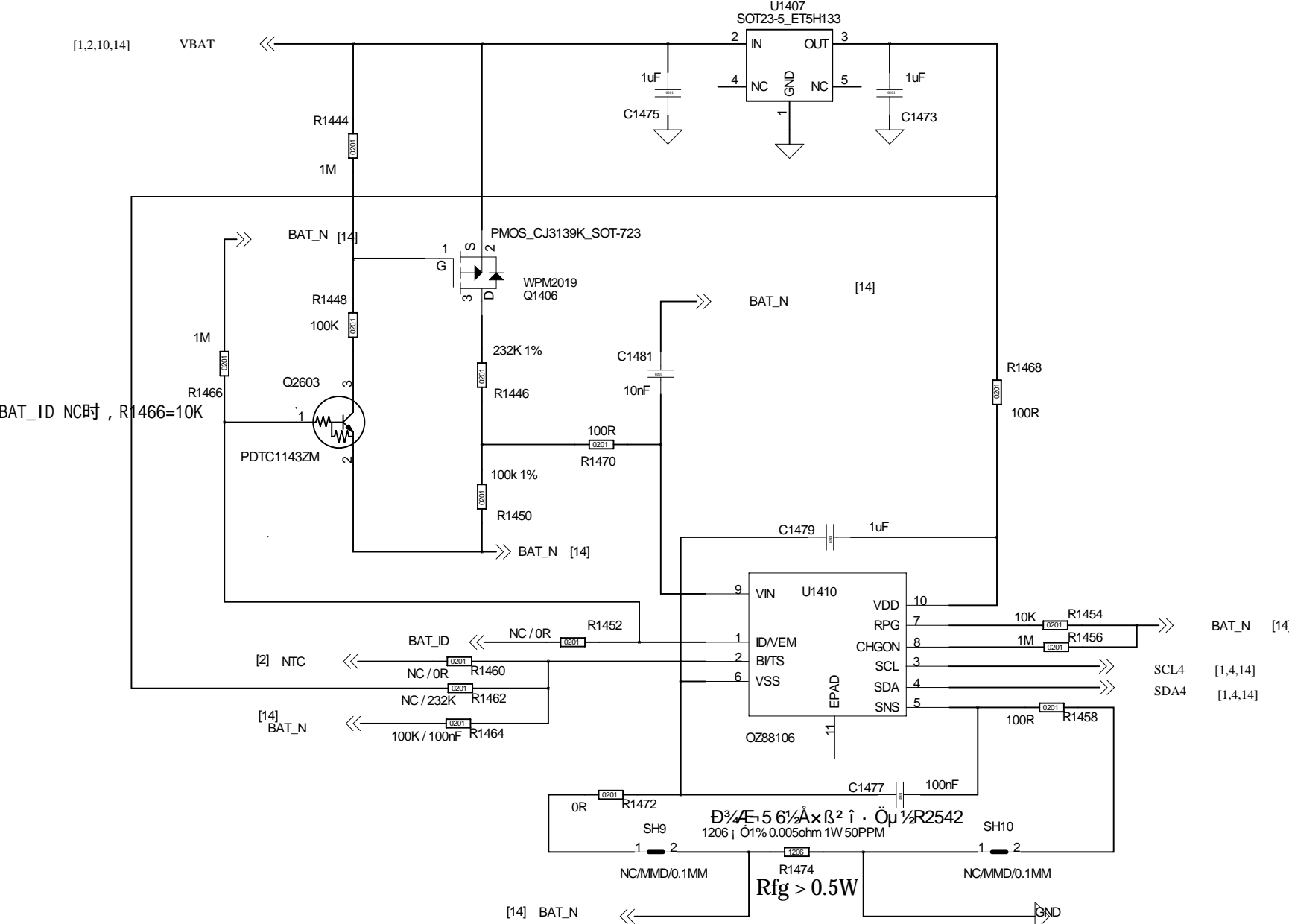


4mm wide



8.4V Double Battery 12V / 1.5A Charger

12V Voltmeter Main Battery



DT02-V1.0修改记录

- 1.M-sensor更换为MMC5603NJ（同POWER3）
- 2.增加音量+ -测试点

DT02-V2.0修改记录

- 1.分离式改为EMCP（DDR4X）
- 2.U5605封装由MSOP8改为SOP8
- 3.RFID SUB预留小板电池上下电GPIO口
- 4.删除射频模块
- 5.删除指纹，hub漏电检测删除，改小PL6410,PL6614,PL6600封装